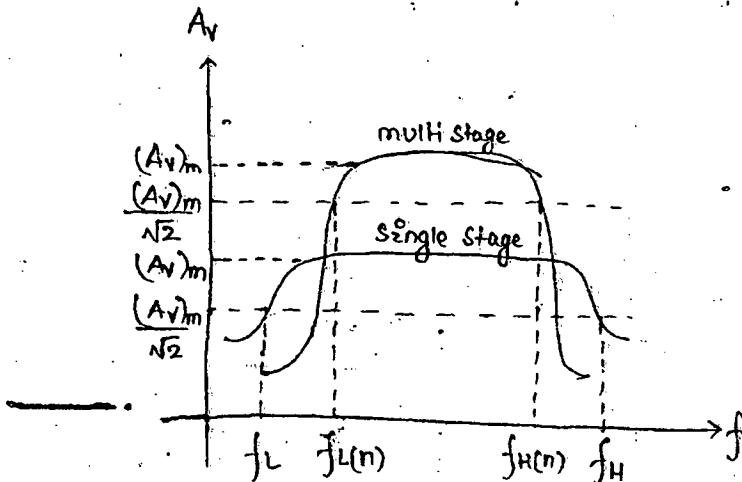


INTEGRATED ELECTRONICS (OP-AMP)

□ MULTI STAGE AMPLIFIERS.

→ EFFECT OF CASCADING ON BANDWIDTH (Identical Stages)



Higher cut-off frequency.

$$(A_v)_h = \frac{(A_v)_m}{1 + j\left(\frac{f}{f_H}\right)}$$

Single Stage

$$\left| \frac{(A_v)_h}{(A_v)_m} \right| = \frac{1}{\sqrt{1 + \left(\frac{f}{f_H}\right)^2}}$$

n identical stage

$$\left| \frac{(A_v)_h}{(A_v)_m} \right|^n = \left| \frac{1}{\sqrt{1 + \left(\frac{f}{f_H}\right)^2}} \right|^n$$

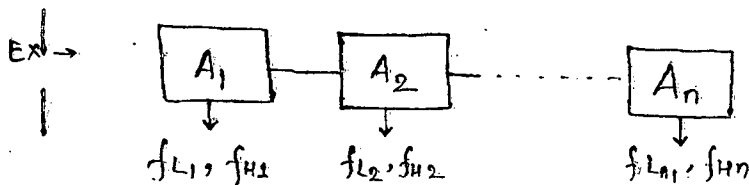
When $f \rightarrow f_{H(n)}$ =

$$\left| \frac{(A_v)_h}{(A_v)_m} \right|^n = \frac{1}{\sqrt{2}} \Rightarrow \frac{1}{\sqrt{1 + \left(\frac{f_{H(n)}}{f_H}\right)^2}} = \frac{1}{\sqrt{2}}$$

$$\Rightarrow f_{H(n)} = f_H \sqrt{2^{1/n} - 1}$$

$$\text{Similarly } f_{L(n)} = \frac{f_L}{\sqrt{2^{1/n} - 1}}$$

→ EFFECT OF CASCADING ON BANDWIDTH (non identical stages)



$$A_L = \frac{(A)_m}{1 - j \left(\frac{f_L}{f_H} \right)}$$

$$A^n = A_1 \cdot A_2 \cdot \dots \cdot A_n$$

$$A_{L_n}^n = A_{L_1} \cdot A_{L_2} \cdot \dots \cdot A_{L_n}$$

$$\sqrt{1 + \left(\frac{f_L^n}{f} \right)^2} = \sqrt{1 + \left(\frac{f_{L1}}{f} \right)^2} \cdot \sqrt{1 + \left(\frac{f_{L2}}{f} \right)^2} \cdot \dots \cdot \sqrt{1 + \left(\frac{f_{Ln}}{f} \right)^2}$$

$$\sqrt{1 + \left(\frac{f_L^n}{f} \right)^2} = \sqrt{1 + \left(\frac{f_{L1}}{f} \right)^2 + \left(\frac{f_{L2}}{f} \right)^2 + \dots + \left(\frac{f_{Ln}}{f} \right)^2 + \left(\frac{f_{L1} \cdot f_{L2}}{f^2} \right)^2 + \dots}$$

$$f_L^n = \sqrt{f_{L1}^2 + f_{L2}^2 + \dots + f_{Ln}^2}$$

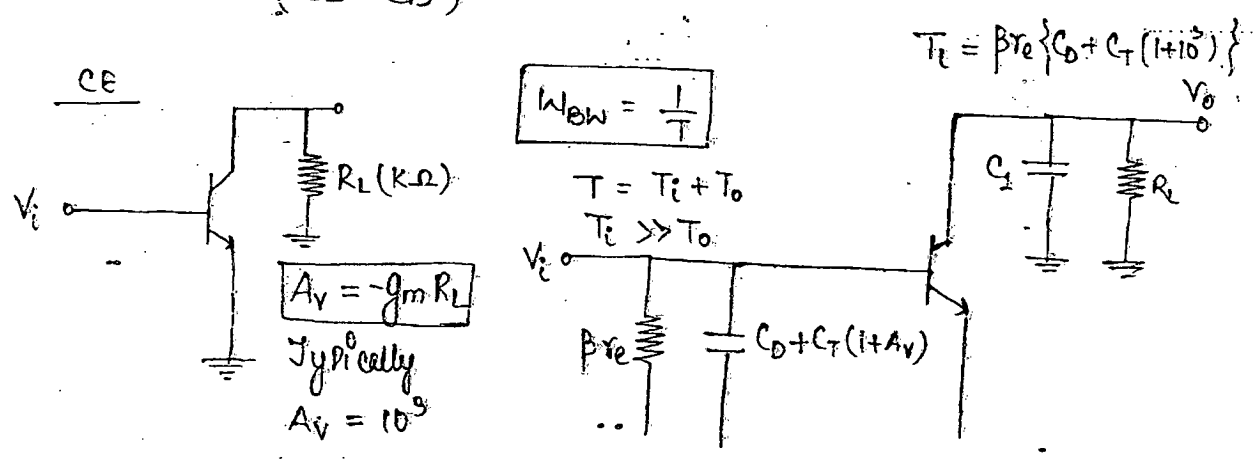
$$f_A^n \approx \frac{1}{\sqrt{\frac{1}{f_{H1}^2} + \frac{1}{f_{H2}^2} + \dots + \frac{1}{f_{Hn}^2}}}$$

← neglected

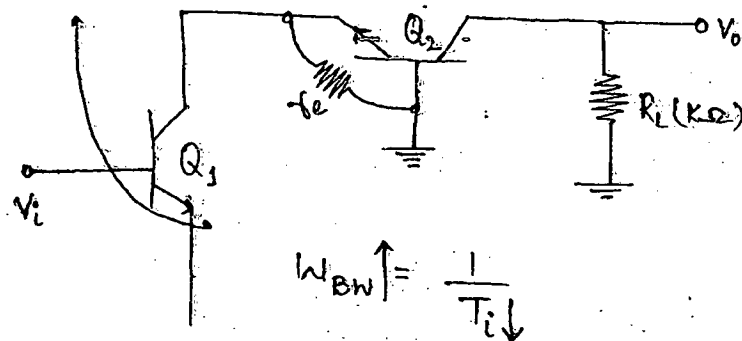
POPULAR CASCADING DESIGN.

1. CASCODE AMPLIFIERS / WIDE BAND AMPLIFIER.
(CE-CB)
2. Darlington pair / High Input Impedance.
(CC-CC)

1. CASCODE AMPLIFIERS / WIDE BAND AMPLIFIER. (CE-CB)



$$T_i = \beta r_e \{ C_D + C_T (1 + \beta) \}$$



$$A_{V1} = -g_m R_L$$

$$= -g_m r_e$$

$$A_{V1} = -1$$

$$A_{V2} = g_m R_L$$

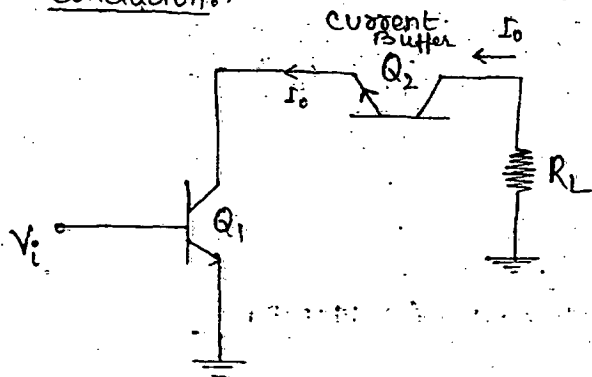
$$|W_{BW}| = \frac{1}{T_i}$$

$$A_{cascode} = A_{V1} \cdot A_{V2}$$

$$= -1 \times g_m R_L$$

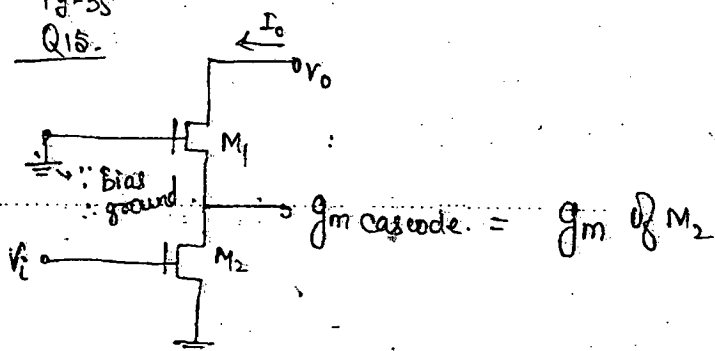
$$A_{cascode} = -g_m R_L$$

Conclusion:-



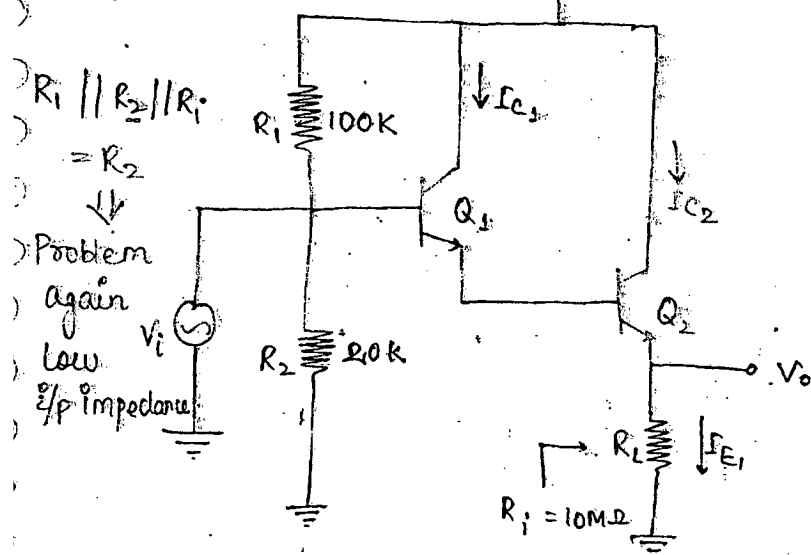
$$g_{m \text{ cascode}} = \frac{I_o}{V_i} = g_{m \text{ CE}}$$

Pg-35
Q15.



$$g_{m \text{ cascode}} = g_m \text{ of } M_2$$

2. DARLINGTON PAIR.



$$1) A_I = A_{I1} \times A_{I2} = \beta^2 (\beta^2 \text{ rule})$$

$$2) A_V = A_{V1} \cdot A_{V2}$$

$$A_{I1} = \frac{I_{E1}}{I_{B1}}, \quad A_{I2} = \frac{I_{E2}}{I_{B2}}$$

$$= 1.1 = 1 (\text{Buffer})$$

$$= \frac{I_{B1} + I_{C1}}{I_{B1}} = 1 + \beta \approx \beta$$

$$3) A_V = A_I \frac{R_L}{R_i}$$

$$1 = A_I \frac{R_L}{R_i}$$

$$R_i = A_I R_L$$

$$= (1 + \beta) \approx \beta$$

$$R_i = A_I R_L [A_I = \beta^2]$$

$$R_i = \beta^2 R_L [\beta = 100]$$

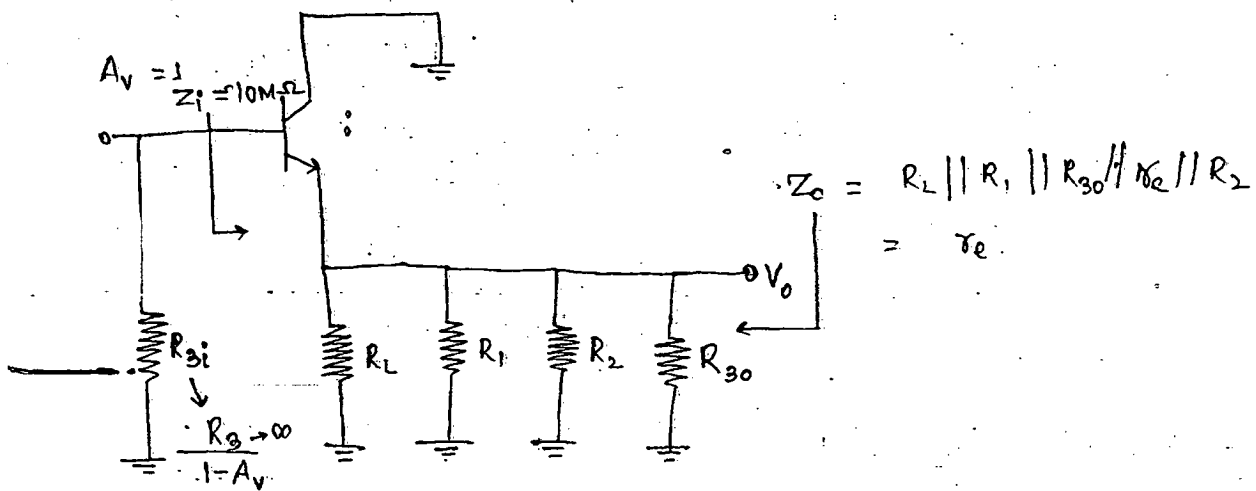
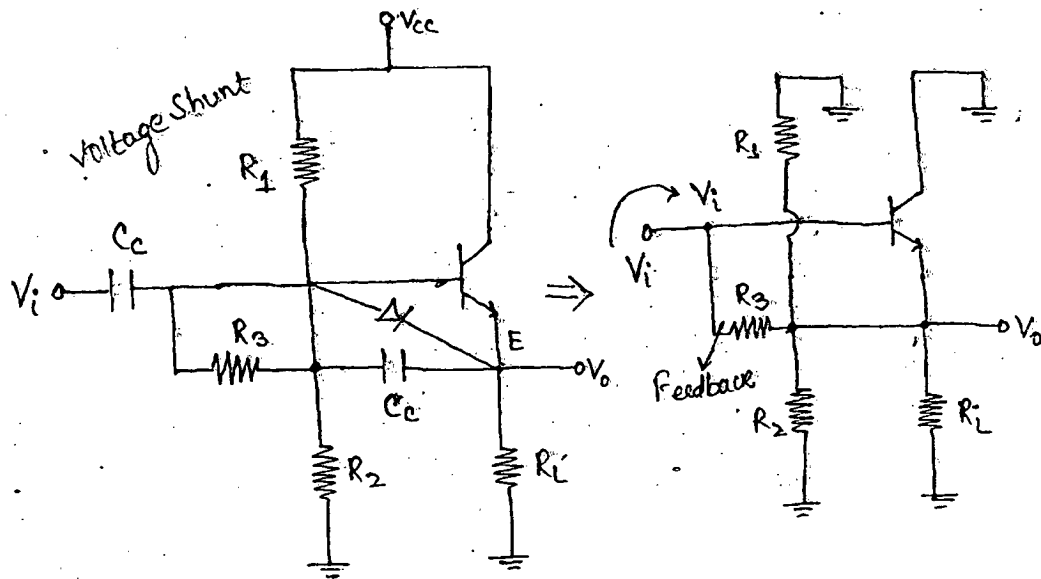
$$R_i = [10 \text{ M}\Omega] [R_L = 1 \text{ m}\Omega]$$

High $\frac{V_i}{i/p}$ impedance.

$$4) Z_o = R_L \parallel r_e$$

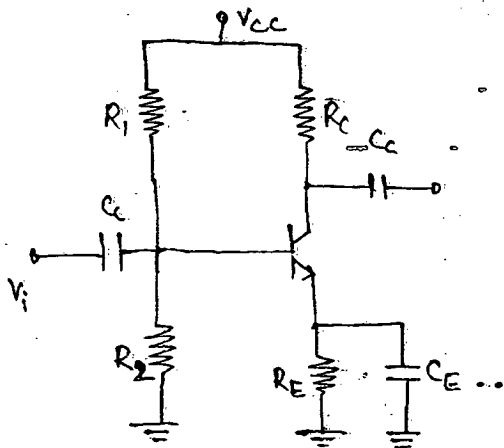
$$= r_e (\text{impedance matching})$$

Modified ckt (Boat Shap Strap Ckt) :-



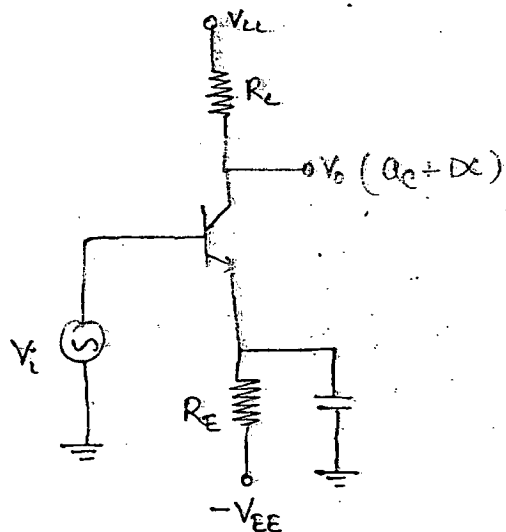
□ COUPLING DESIGN.

1. RC COUPLED BJT AMPLIFIERS.



- * Low gain
- * Size is more

2. DIRECT COUPLED BJT AMPLIFIERS.



- * High gain
- * Small size.

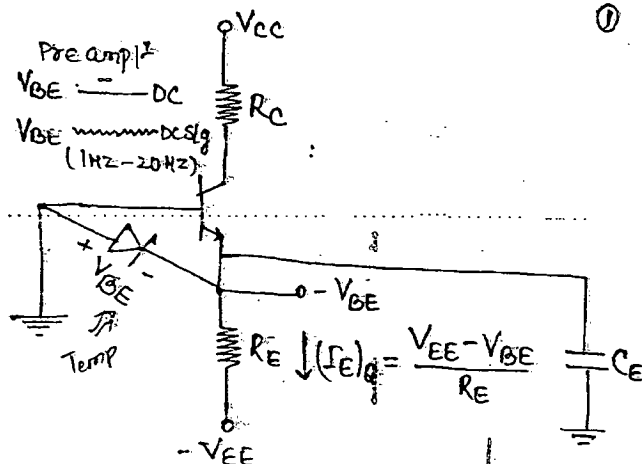
INTEGRATED AMPLIFIERS.

Requirement of integrated amplifier

- (1) High gain (2) Small size.

□ DIRECT COUPLED BJT AMPLIFIER (PROBLEMS).

DC Model



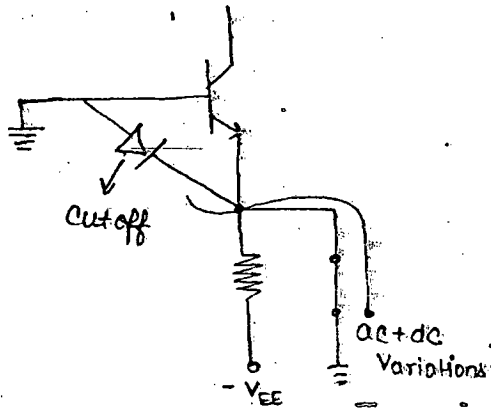
$$\textcircled{1} \frac{dV_{BE}}{dT} = -2.5 \text{ mV}/^{\circ}\text{C}$$

$$\text{Unstable } (I_E)_Q = \frac{V_{EE} - V_{BE}}{R_E} \leftarrow \text{Temp}$$

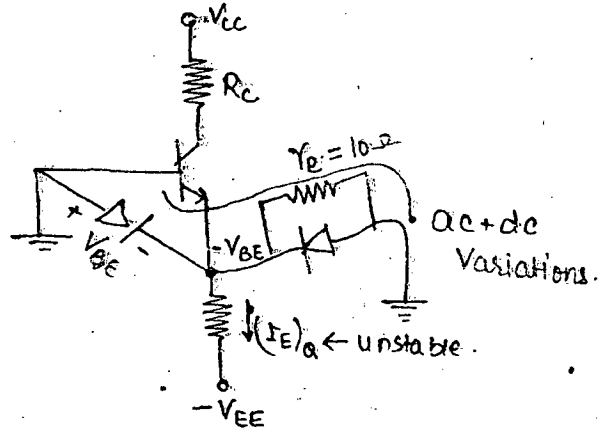
$$\textcircled{2} f_{min} = \frac{1}{2\pi C_E R_E} \text{ (DC signal)}$$

↓
1 Hz. ↑
 ∞

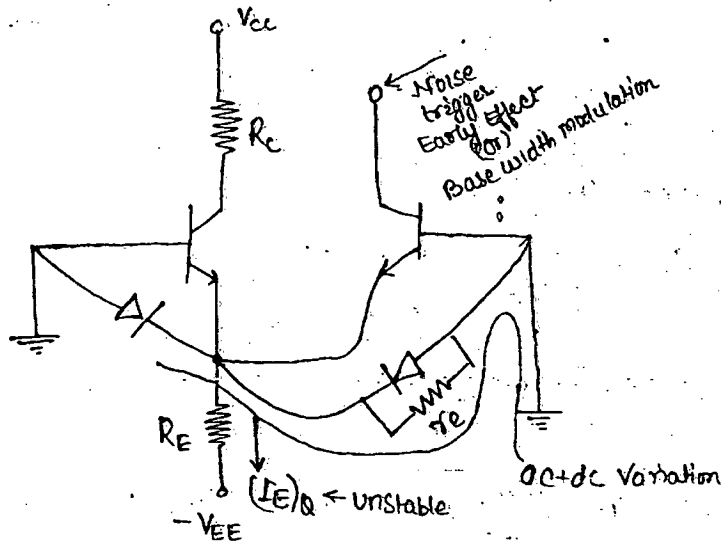
Method - 1



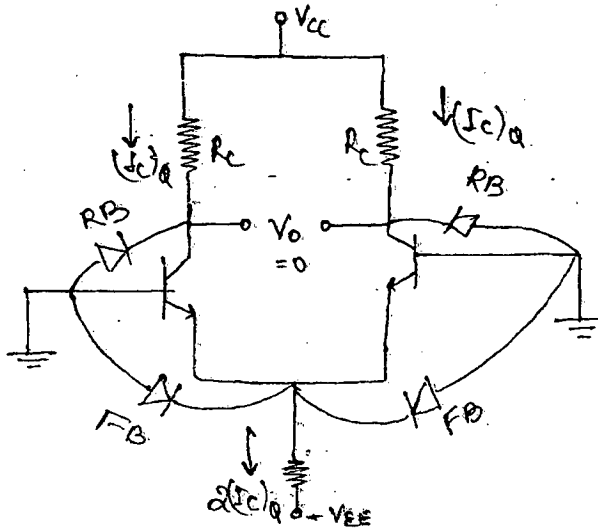
METHOD-2



METHOD-3

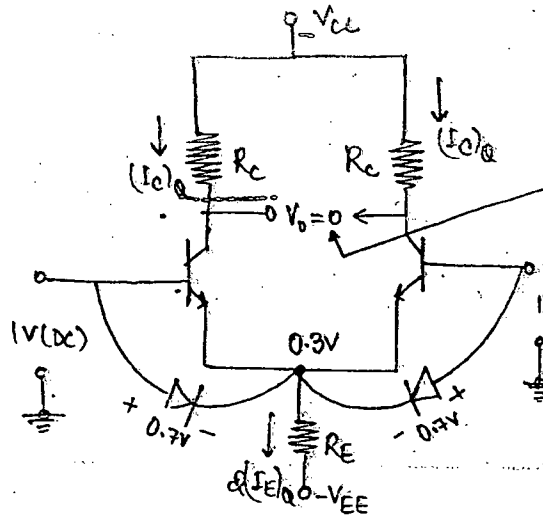


METHOD-4 . (DIFFERENTIAL AMPLIFIER)



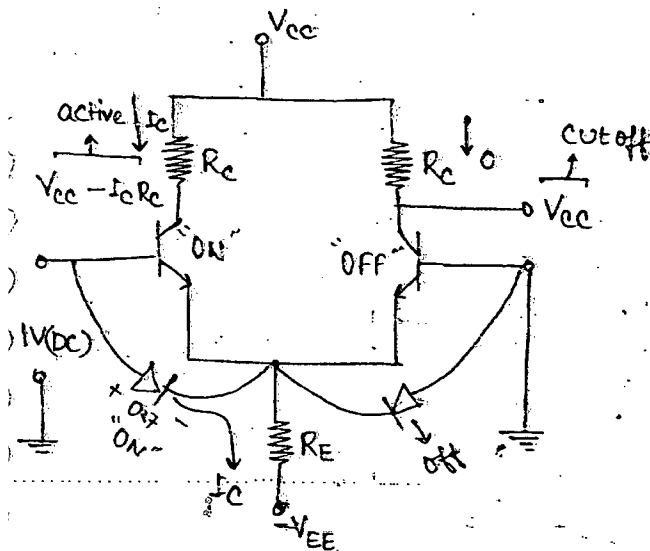
TESTING OF DIFFERENTIAL AMPLIFIER.

Common Input $\Rightarrow$



Common signals (or) DC signal (or) Interference signal (or) Noise signals are eliminated by differential amplifiers.

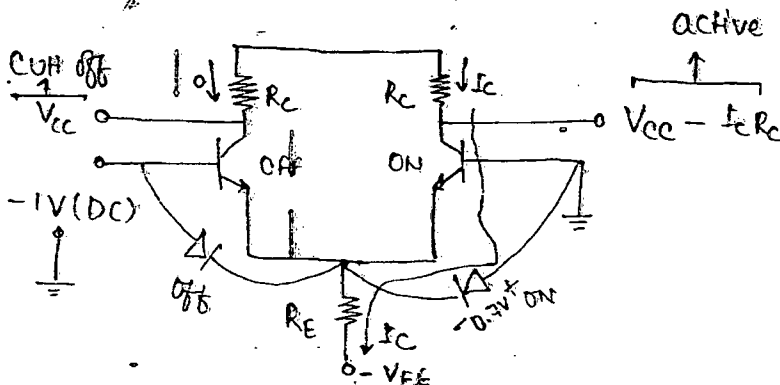
Large Difference Input $\Rightarrow$



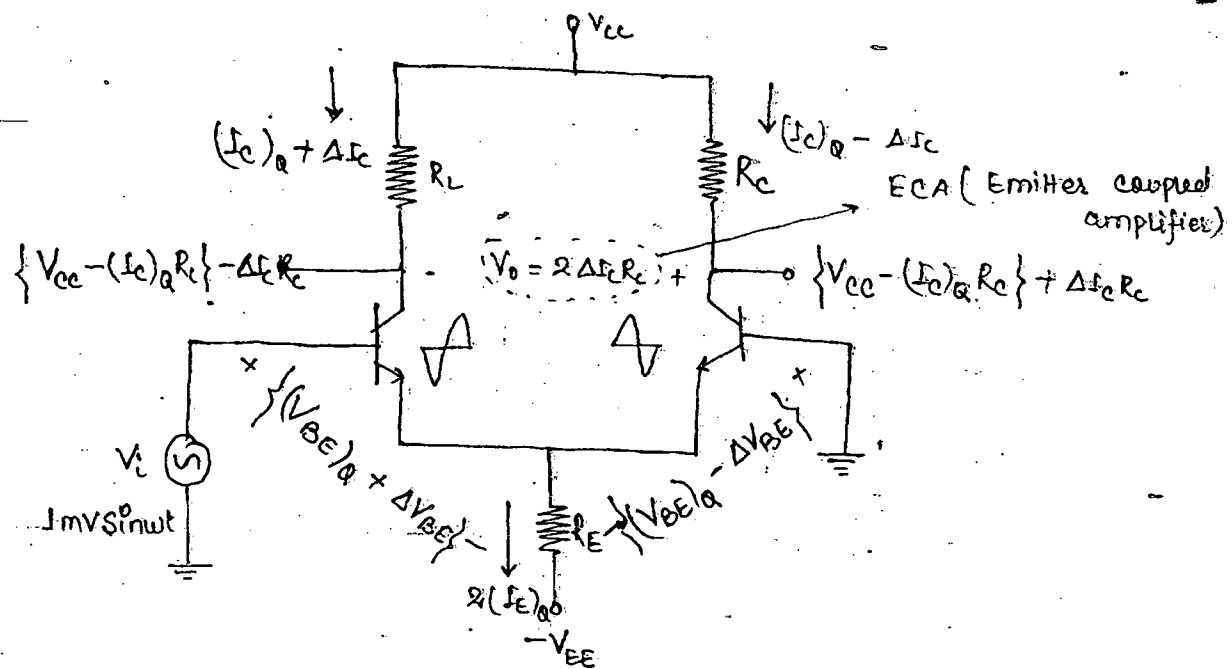
ECL (Emitter coupled logic)
(or)

CML (Current mode logic)

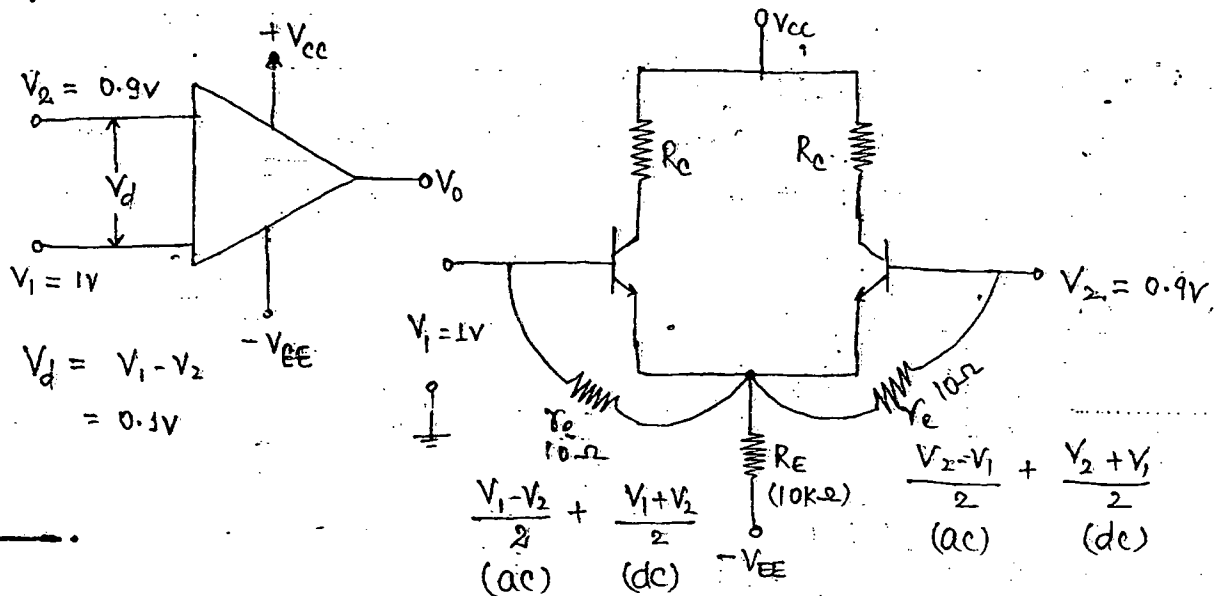
$\Downarrow$
Fastest Switch.



Small Difference i/p



□ MATHEMATICAL ANALYSIS OF DIFFERENTIAL AMPLIFIER.



$$V_1 = \frac{V_1 - V_2}{2} + \frac{V_1 + V_2}{2}$$

$$= \frac{1 - 0.9}{2} + \frac{1 + 0.9}{2}$$

$$= \frac{0.1}{2} + 0.95$$

(ac) (dc)

$$V_2 = \frac{V_2 - V_1}{2} + \frac{V_2 + V_1}{2}$$

$$= \frac{0.9 - 1}{2} + \frac{0.9 + 1}{2}$$

$$= -\frac{0.1}{2} + 0.95$$

(ac) (dc)

$$V_d = V_1 - V_2$$

$$= \frac{0.1}{2} + 0.95 - \left(-\frac{0.1}{2}\right) - 0.95$$

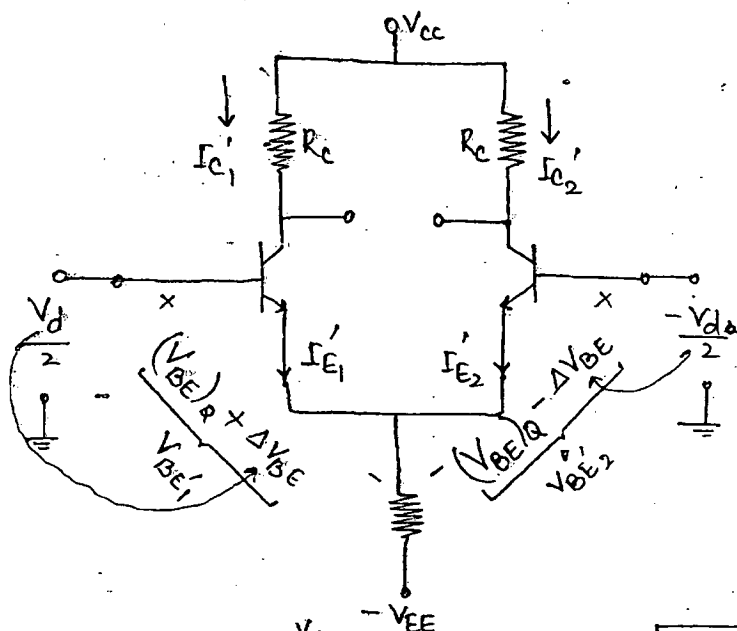
$$= 0.1V$$

conclusion:- $V_d = V_1 - V_2$ (Differential voltage).

$V_c = \frac{V_1 + V_2}{2}$ (Common mode voltage).

$$V_1 = \frac{V_d}{2} + V_c ; V_2 = -\frac{V_d}{2} + V_c$$

□ TRANSFER CHARACTERISTICS OF OP-AMP.



$$I_{E1}' = (I_E)_Q + \Delta I_E$$

$$I_{E2}' = (I_E)_Q - \Delta I_E$$

$$V_{BE1}' = (V_{BE})_Q + \Delta V_{BE}$$

$$V_{BE2}' = (V_{BE})_Q - \Delta V_{BE}$$

$$I_{E1}' = I_{E0} e^{V_{BE1}'/V_T}$$

$$I_{E2}' = I_{E0} e^{V_{BE2}'/V_T}$$

$$\frac{I_{E1}'}{I_{E2}'} = e^{(V_{BE1}' - V_{BE2}')/V_T} \Rightarrow \frac{I_{E1}'}{I_{E2}'} = e^{V_d/V_T}$$

$$\frac{x}{y} = r \Rightarrow \frac{x-y}{x+y} = \frac{r-1}{r+1}$$

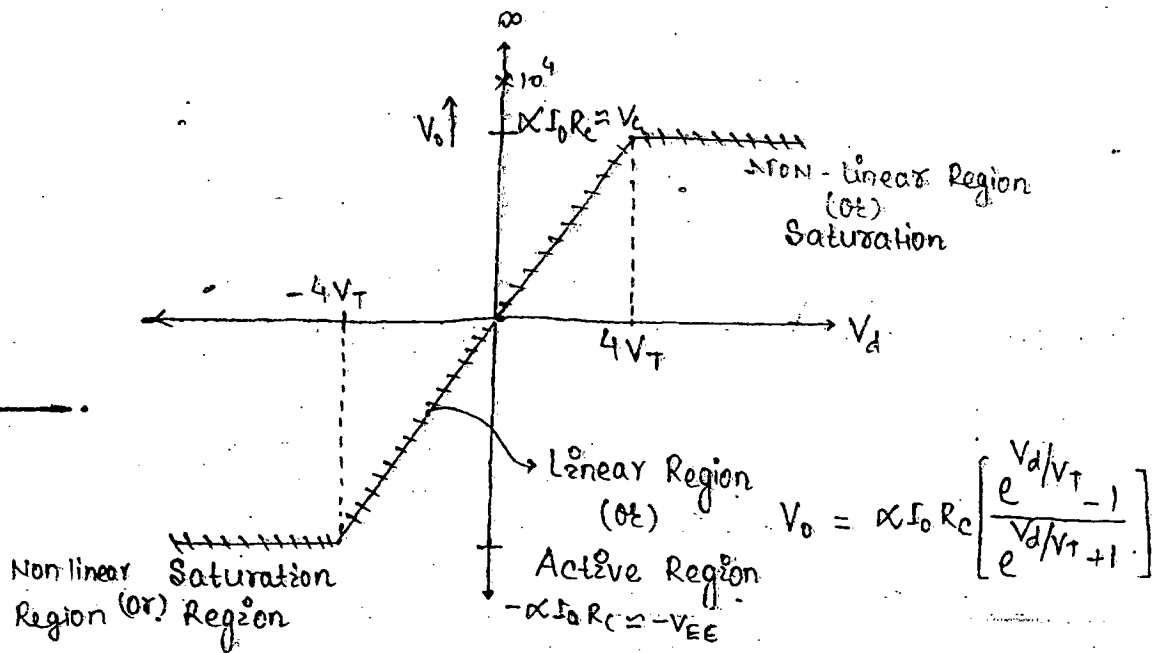
$$V_d = V_T \ln \left(\frac{I_{E1}'}{I_{E2}'} \right) ; \frac{I_{E1}' - I_{E2}'}{I_{E1}' + I_{E2}'} = \frac{[e^{V_d/V_T} - 1]}{[e^{V_d/V_T} + 1]}$$

$$I_{E1}' - I_{E2}' = \frac{I_{C1}' - I_{C2}'}{\alpha}$$

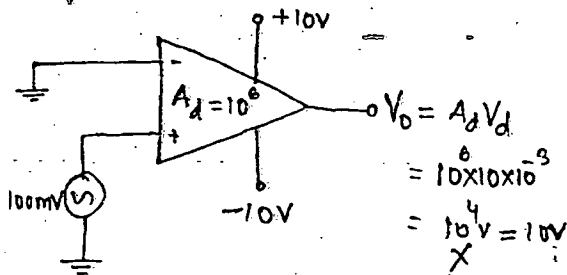
$$I_{E1}' + I_{E2}' = I_0(\alpha)$$

$$\frac{R_C (I_{C1}' - I_{C2}')}{\alpha I_0} = \frac{(e^{V_d/V_T} - 1) R_C}{e^{V_d/V_T} + 1}$$

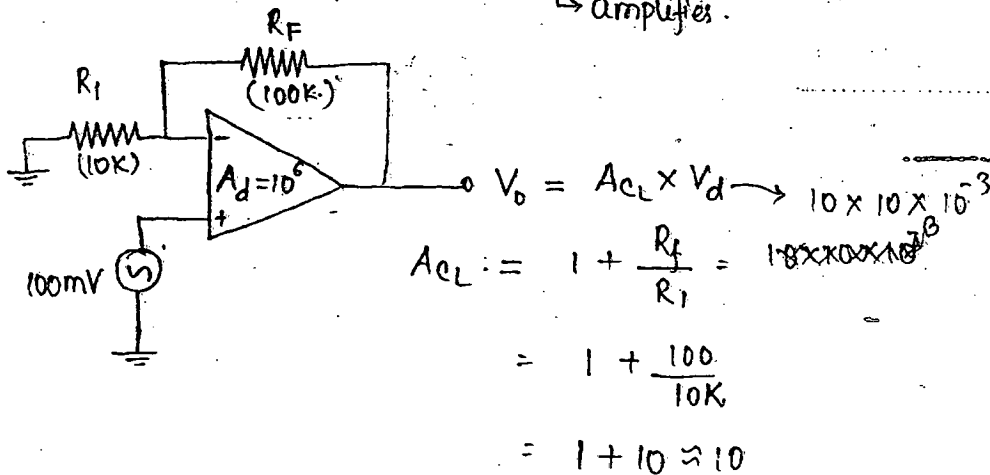
$$V_o = \frac{\alpha I_0 R_C (e^{V_d/V_T} - 1)}{(e^{V_d/V_T} + 1)}$$



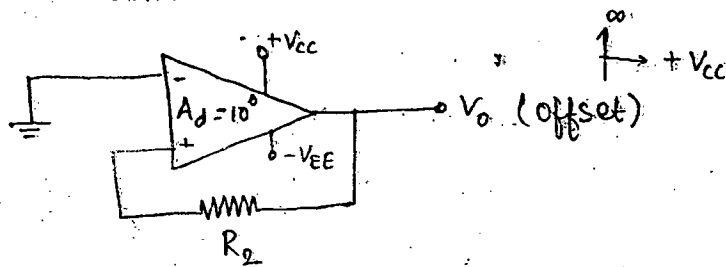
Open loop Opamp (Non-Linear)



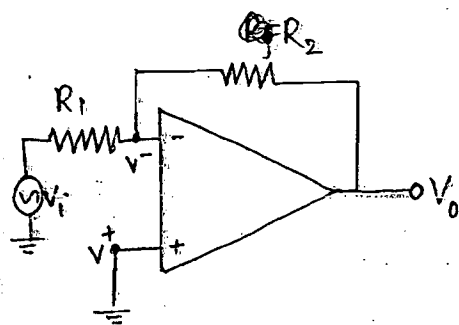
Negative feedback (Linear) $\rightarrow$ amplifier.



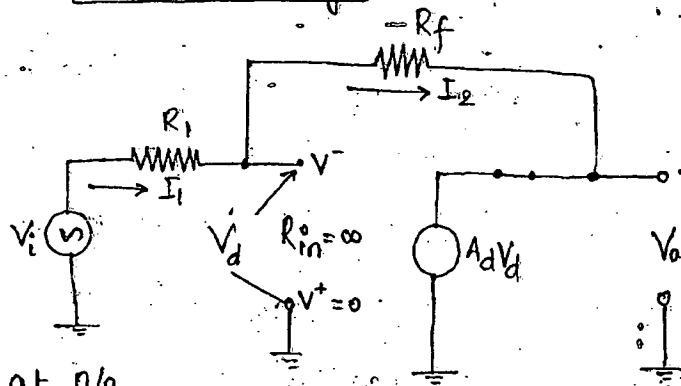
Positive Feedback (Non-Linear)



VIRTUAL GROUND THEORY (VALID FOR NEGATIVE FEEDBACK)



Ideal Theory



KCL at i/p

$$I_1 = I_2$$

$$\frac{V_i - V^-}{R_1} = \frac{V^- - V_o}{R_2}$$

$$\frac{V_i - 0}{R_1} = \frac{0 - V_o}{R_2}$$

$$\boxed{\frac{V_o}{V_i} = -\frac{R_2}{R_1}}$$

KVL at o/p

$$V_o = A_d V_d$$

$$V_d = \frac{V_o}{A_d} = \frac{V_o}{\infty} = 0$$

$$V_d = V^+ - V^- = 0$$

$$\boxed{V^+ = V^-}$$

Ideal

Practical

$$V_o \approx A_d V_d$$

$$V_d \approx \frac{V_o}{A_d} \approx \frac{V_o}{10^6} \approx 0$$

$$V_d \approx V^+ - V^- \approx 0$$

$$\boxed{V^+ \approx V^-}$$

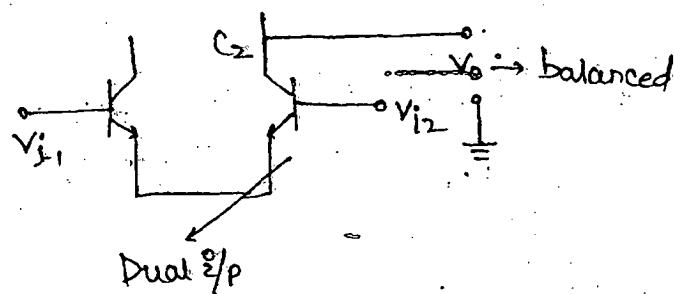
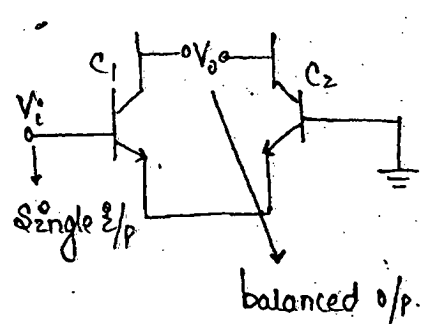
CONCLUSION :- Virtual Ground is acceptable only for (-ve) feedb.

DIFFERENTIAL & AMPLIFIER CONFIGURATION.

At the i/p side, we have :- $\rightarrow$ Single i/p
 $\rightarrow$ Dual i/p

At the o/p side, we have :- $\rightarrow$ Balanced o/p
 $\rightarrow$ unbalanced o/p

- Configurations :-
- Single i/p balanced o/p.
 - Single i/p unbalanced o/p
 - Dual i/p balanced o/p.
 - Dual i/p unbalanced o/p



□ PROBLEMS IN DIFFERENTIAL AMPLIFIERS.

Q1. Calculate the voltage gain of single i/p balanced o/p (or) Dual i/p balanced o/p differential amplifier with $R_C = 1k\Omega$ and $(I_E)_Q = 26mA$.

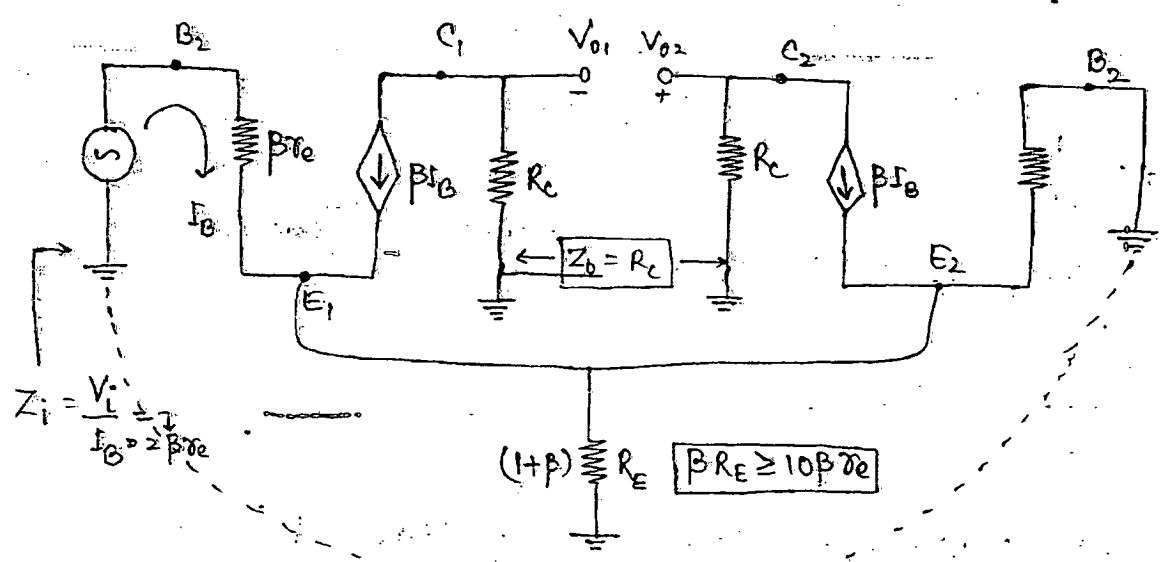
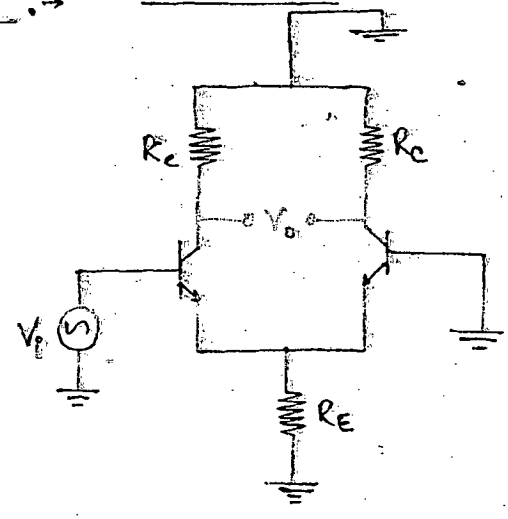
Q2. Calculate the voltage gain of single i/p unbalanced o/p (or) Dual i/p unbalanced o/p differential amplifier with $R_C = 1k\Omega$ and $(I_E)_Q = 26mA$.

Q3. Calculate the i/p Impedance of a differential amplifier with $\beta = 50$ and $(I_E)_Q = 26mA$.

Q4. Calculate the o/p Impedance of a differential amplifier with $R_C = 1k\Omega$.

Q5. Design the operating point for a differential amplifier ckt.

9.4.2 $\Rightarrow$ AC ANALYSIS



$$V_{o1} = -\beta I_B R_c$$

$$= -\beta \frac{V_i}{2\beta r_e} \cdot R_c$$

$$A_{d1} = \frac{V_{o1}}{V_i} = -\frac{R_c}{2r_e}$$

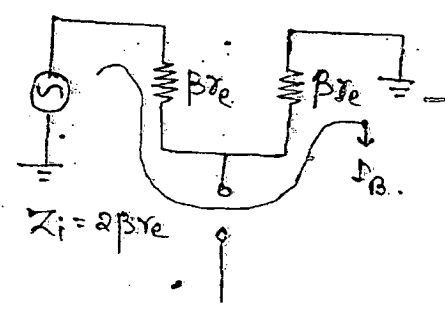
Unbalanced gain

$$A_d = \frac{R_c}{r_e}$$

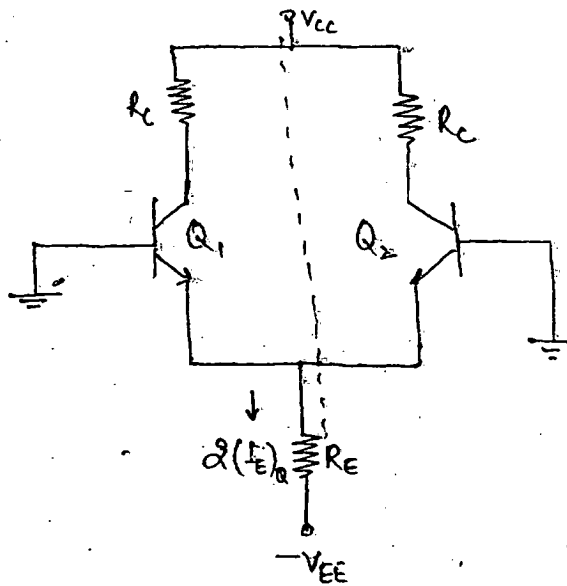
Balanced gain

$$A_{d2} = \frac{V_{o2}}{V_i} = +\frac{R_c}{2r_e}$$

Unbalanced gain



DC ANALYSIS



Q1

$$V_{EE} = V_{BE} = -2(I_E)_Q R_E$$

$$(I_E)_Q = \frac{V_{EE} - V_{BE}}{2R_E}$$

Q1. Balanced o/p

$$A_d = \frac{R_c}{r_e}$$

$$r_e = \frac{V_i}{(I_E)_Q} = \frac{26\text{mV}}{26\text{mA}} = 1\Omega$$

$$A_d = 10^3$$

Q2.

$$Z_i = \beta r_e$$

$$= 2 \times 50 \times 1$$

$$= 100\Omega$$

Drawbacks :- 1. Voltage gain is not stable because of the dynamic resistance r_e .

2. The i/p Impedance is low.

→ Swamping Resistor Technique :-

$$A_d = \frac{R_c}{r_e} \text{ (unstable)}$$

↓
Swamping Technique

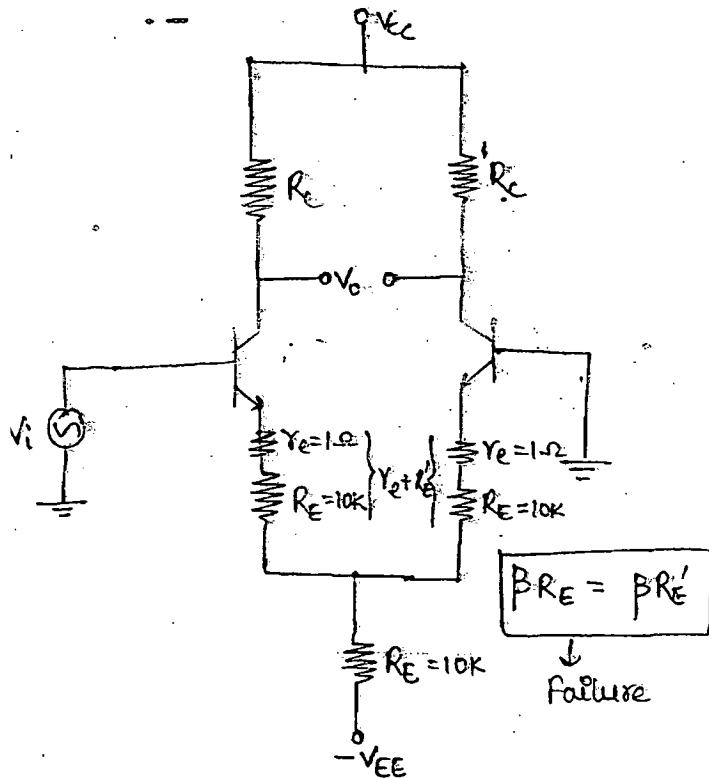
$$r_e \rightarrow r_e + R'_E$$

$$A_d = \frac{R_c}{r_e + R'_E}$$

$$R'_E \gg r_e$$

$$A_d = \frac{R_c}{R'_E}$$

→ Stable.



$$Z_i = 2\beta r_e \text{ (Low Impedance)}$$

Swamping technique

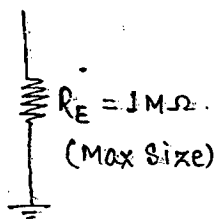
$$r_e \rightarrow r_e + R'_E$$

$$Z_i = 2\beta R'_E$$

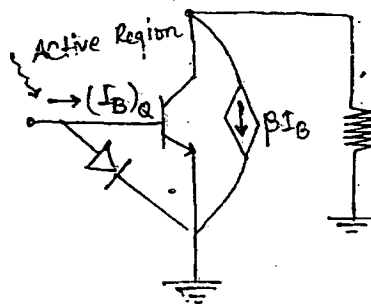
$$= 2 \times 50 \times 10k\Omega$$

$$= 1M\Omega \text{ (high i/p impedance)}$$

> Replacing Resistor R_E by a transistor which gives Resistance of $1M\Omega$.

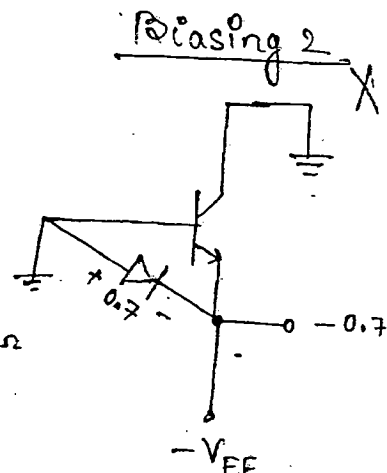
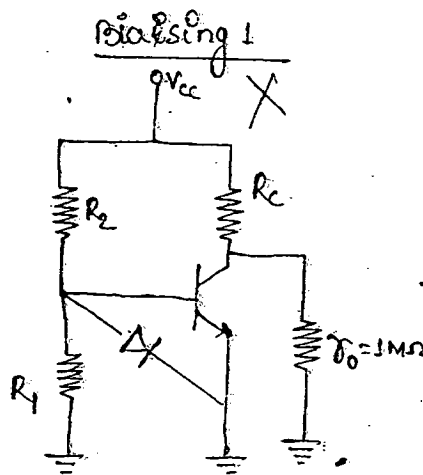
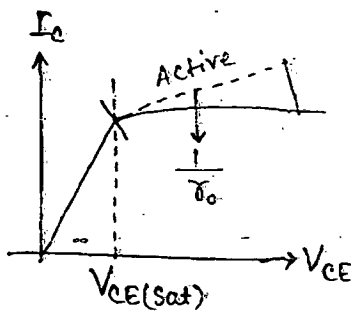


$\Rightarrow$

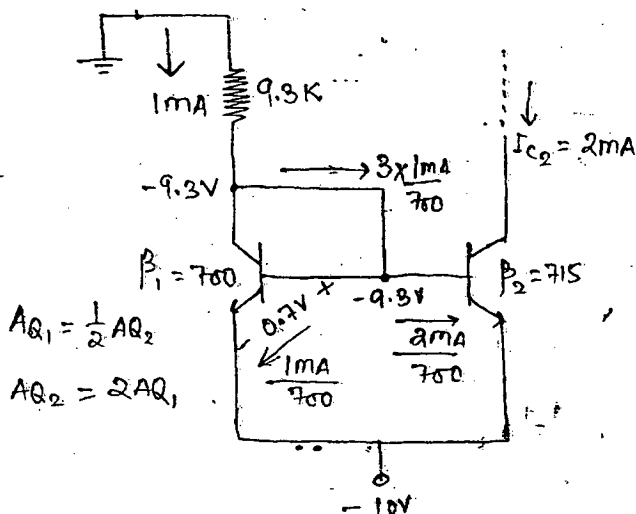
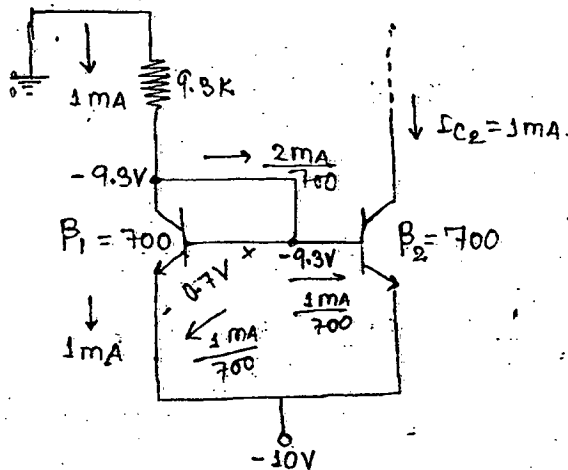
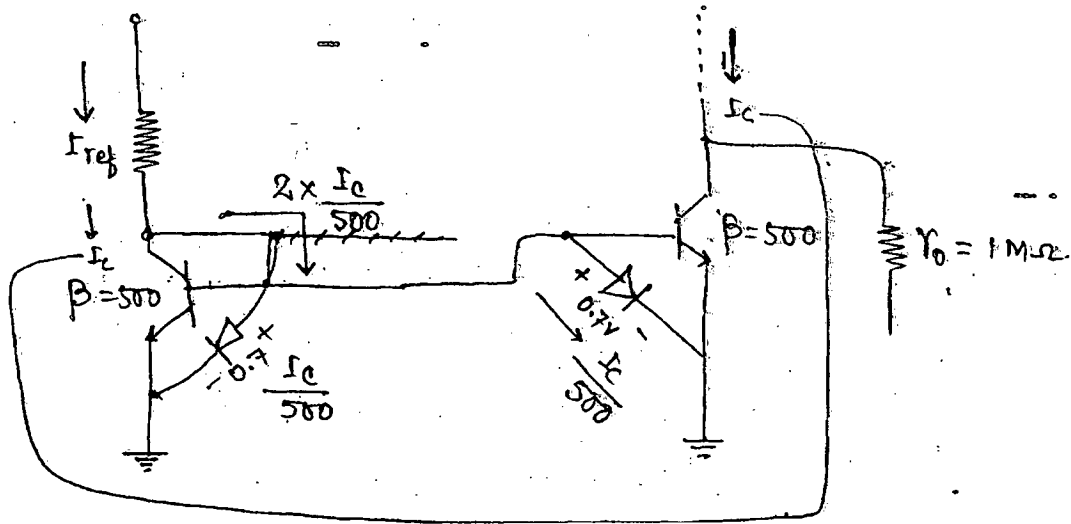


$$r_o = \frac{V_A}{(I_C)_Q} = 1M\Omega$$

Suppose $V_A = 1000$
 $(I_C)_Q = 1mA$



□ CURRENT MIRROR BIASING. (VLSI)



$$Q_1 \quad \begin{array}{|c|c|c|} \hline A_E & A_B & A_C \\ \hline \end{array}$$

$$Q_2 \quad \begin{array}{|c|c|c|} \hline 2A_E & 2A_B & 2A_C \\ \hline \end{array}$$

$$I_E = I_0 e^{V_{BE}/V_T} \rightarrow Q_1$$

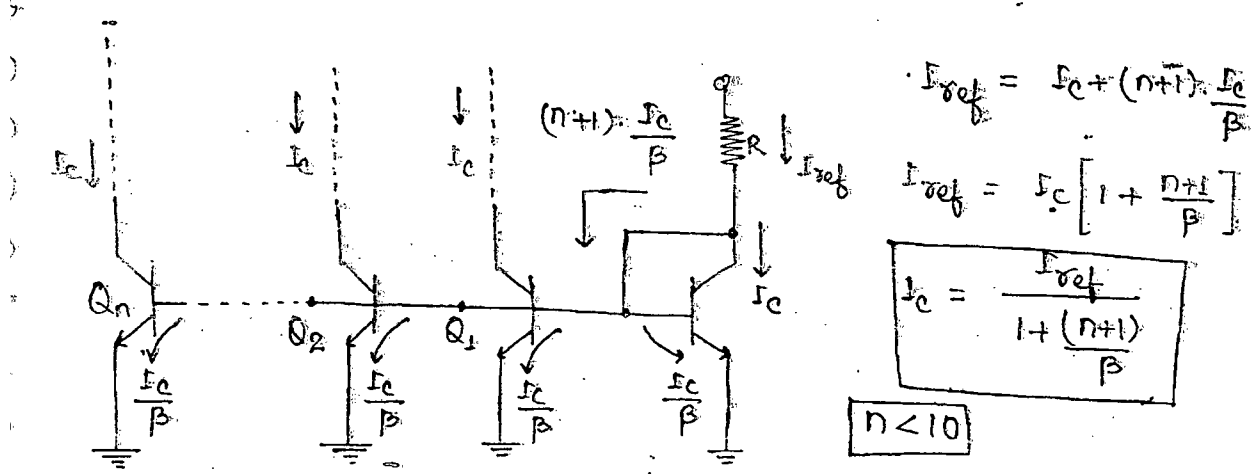
$$2I_E = 2I_0 e^{V_{BE}/V_T} \rightarrow Q_2$$

$$I_0 = Aq \left[\frac{D_p}{L_p N_D} + \frac{D_n}{L_n N_A} \right] n_i$$

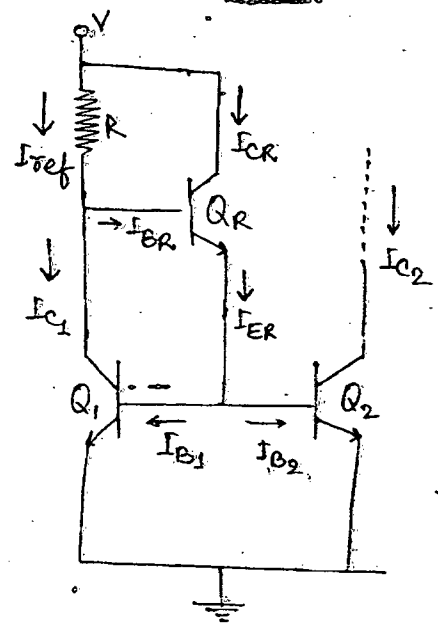
$$I_0 \propto \text{Area.}$$

$$2I_0 \propto 2 \text{ Area.}$$

□ DRAWBACKS OF SINGLE CURRENT MIRROR.



□ MODIFIED CURRENT MIRROR.



$$\begin{aligned}
 I_{ref} &= I_{C1} + I_{BR} \\
 &= I_{C1} + \frac{I_{ER}}{1+\beta} \\
 &= I_{C1} + \frac{I_{B1} + I_{B2}}{1+\beta}
 \end{aligned}$$

$n > 10$

Current mirror is

$$Q_1 = Q_2$$

$$I_{C1} = I_{C2} ; I_{B1} = I_{B2} ; \beta_1 = \beta_2$$

$$\begin{aligned}
 I_{ref} &= I_{C2} + \frac{2 I_{B2}}{1+\beta} \\
 &= I_{C2} + \frac{2 I_{C2}}{\beta(1+\beta)}
 \end{aligned}$$

$$\Rightarrow I_{ref} = I_{C2} \left[1 + \frac{2}{\beta + \beta^2} \right] \Rightarrow$$

$$I_{C2} = \frac{I_{ref}}{1 + \frac{2}{\beta^2}}$$

$$I_{Ci} = \frac{I_{ref}}{1 + \frac{(n+1)}{\beta^2}} \quad i = 1, 2, 3, \dots, n$$

□ WIDELAR CURRENT SOURCE.

$$I_{ref} \rightarrow I_{C2}$$

$$V_{BE1} = V_{BE2} + I_{C2} R_E$$

$$I_{C2} R_E = V_{BE1} - V_{BE2}$$

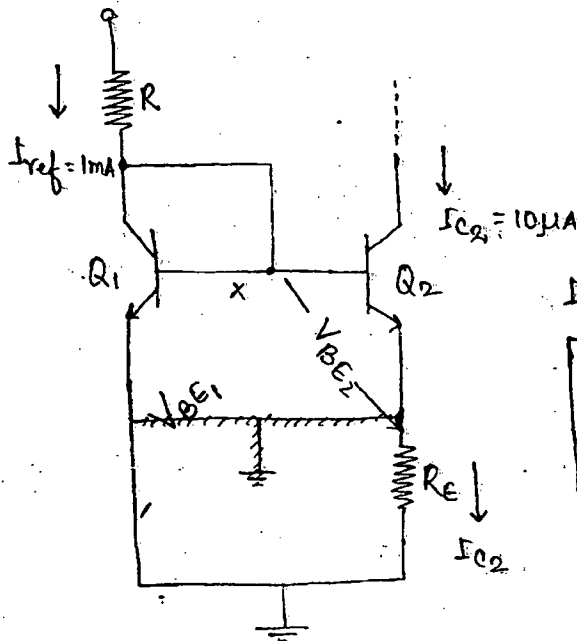
$$V_{BE1} = V_T \ln \left(\frac{I_{ref}}{I_0} \right)$$

$$V_{BE2} = V_T \ln \left(\frac{I_{C2}}{I_0} \right)$$

$$I_{C2} R_E = V_T \ln \left(\frac{I_{ref}}{I_{C2}} \right)$$

$$R_E = \frac{V_T \ln \left(\frac{I_{ref}}{I_{C2}} \right)}{I_{C2}}$$

$$R_E = \frac{0.026 \ln \left(\frac{1mA}{10\mu A} \right)}{10\mu A} = 1.2 \text{ k}\Omega$$



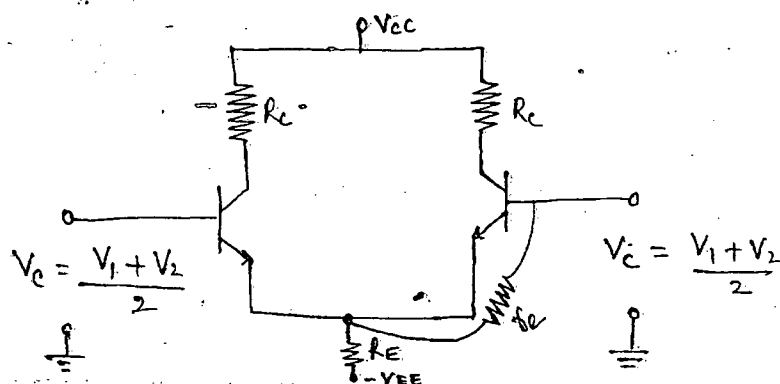
□ CMRR (COMMON MODE REJECTION RATIO):

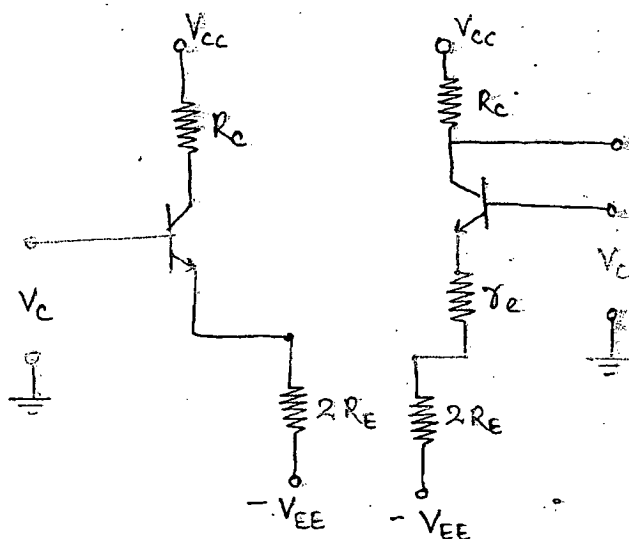
$$CMRR = \frac{A_d}{A_c} = 10^6$$

$$|CMRR|_{dB} = 20 \log \frac{A_d}{A_c} = 20 \log 10^6$$

$$|CMRR|_{dB} = 120dB$$

COMMON MODE GAIN AC





$$A_c = \frac{-R_c}{r_{e1} + 2R_E}$$

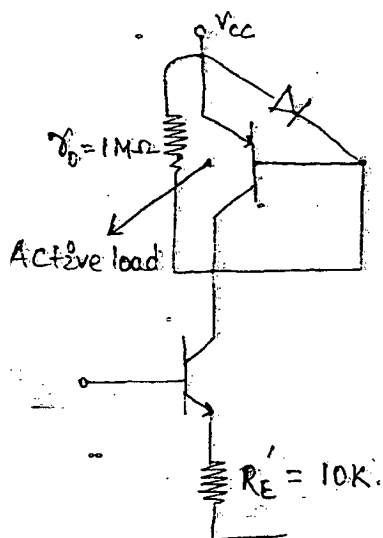
$$A_c = \frac{-R_c}{2R_E} \rightarrow \text{unbalanced}$$

$$A_d = \frac{-R_c}{2r_{e1}} \rightarrow \text{unbalanced}$$

$$CMRR = \frac{A_d}{A_c} = \frac{-R_c/2r_{e1}}{-R_c/2R_E} = \frac{R_E}{r_{e1}}$$

$$CMRR = \frac{R_E}{r_{e1}} = g_m R_E$$

□ DIFFERENTIAL GAIN IMPROVEMENT BY ACTIVE LOAD.



Passive load R_c .

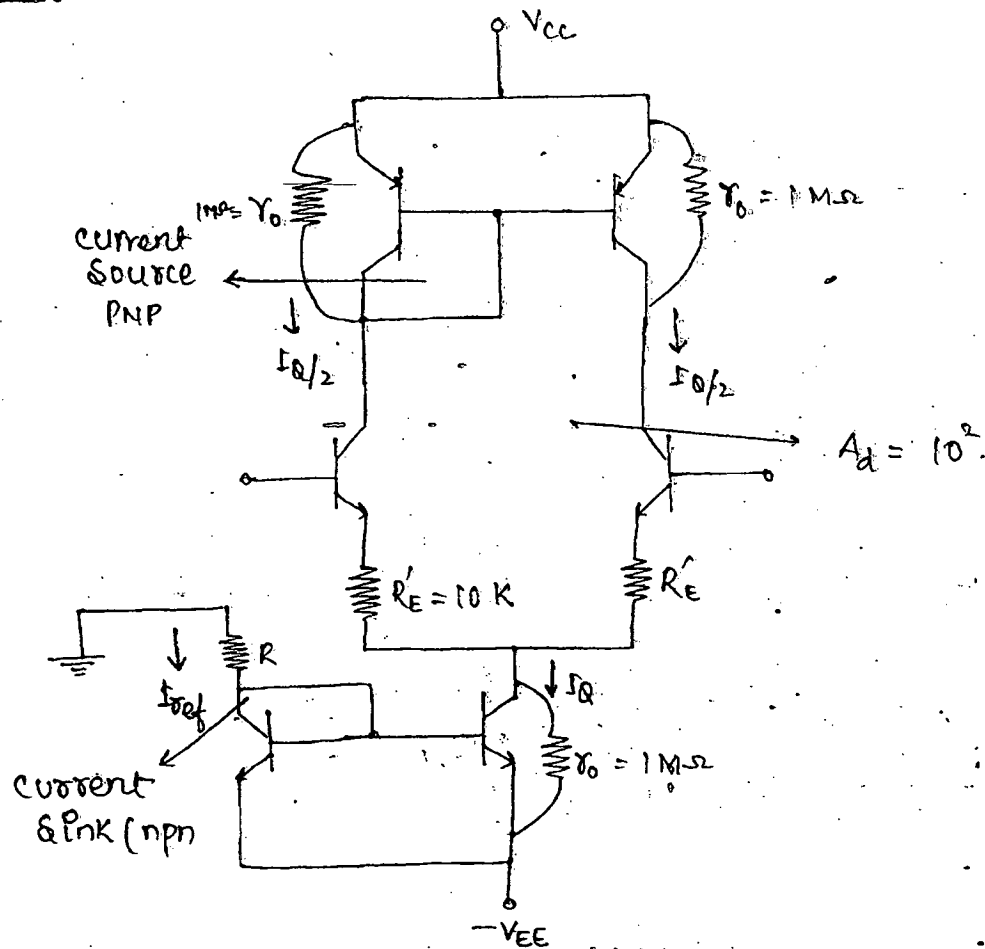
$$A_d = \frac{R_c}{R_E'} = \frac{10K}{10K\Omega} = 1.$$

Active load r_o .

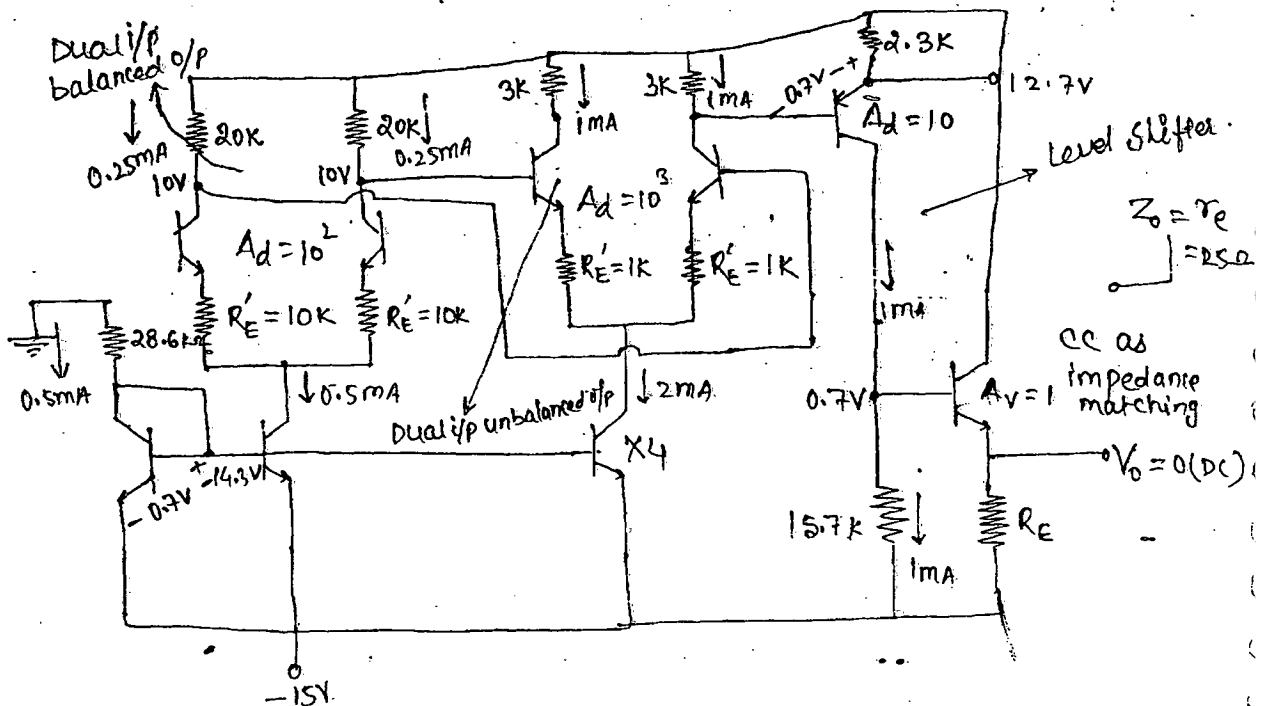
$$A_d = \frac{R_c}{R_E'} = \frac{r_o}{R_E'} = \frac{1M\Omega}{10K\Omega} = 100.$$

□ INTEGRATED IC DIFFERENTIAL AMPLIFIER

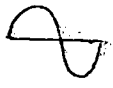
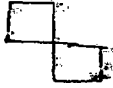
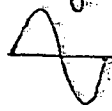
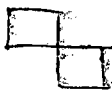
□ INTEGRATED IC DIFFERENTIAL AMPLIFIER



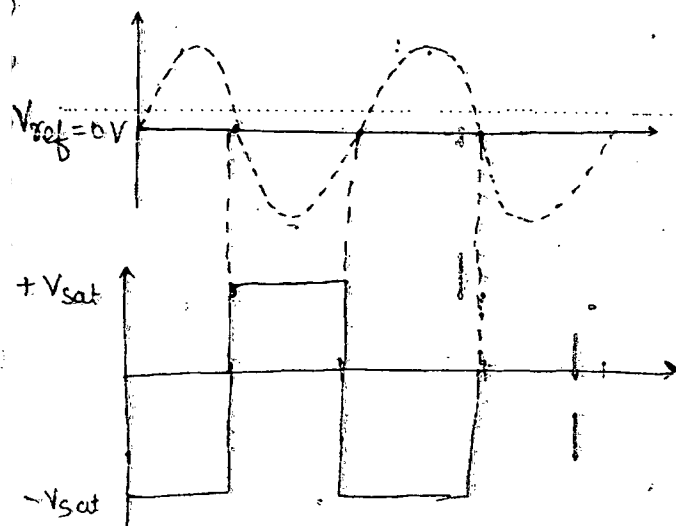
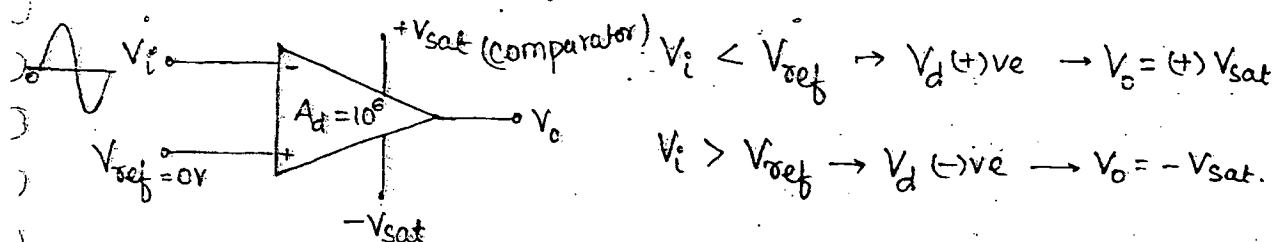
□ TYPICAL DESIGN OF AN OPAMP.



□ NON LINEAR APPLICATIONS.

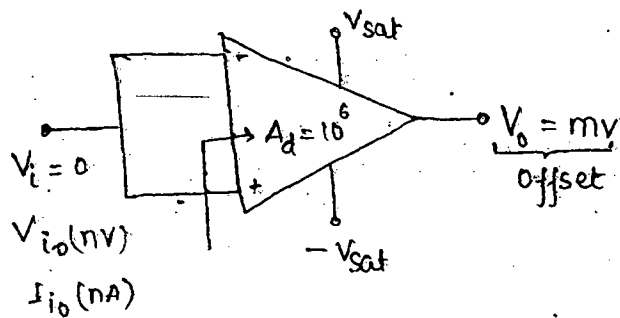
- ZCD (Zero Crossing Detector).  to 
- Schmitt Trigger.  to 
- Bistable Trigger Multivibrator (Flip Flop).
- Astable Multivibrator (Oscillator). Function Generator.
- RC Oscillators → (a) RC phase shift oscillator } audio frequency oscillator
(b) Wein bridge oscillators }
- LC Oscillators → (a) Hartley } Radio frequency oscillator
(b) Colpits }
- Monostable Multivibrator. (Pulse generator)
- 555 timer.

□ ZCD (ZERO CROSSING DETECTOR)



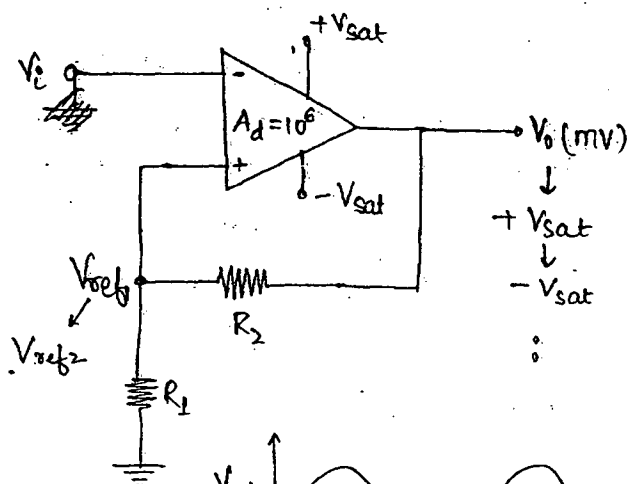
SCHMITT TRIGGER

OFFSET



$V_{io} = \text{I/P offset voltage}$

$I_{io} = \text{I/P offset current}$



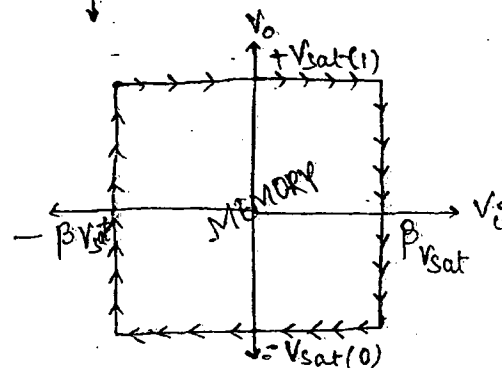
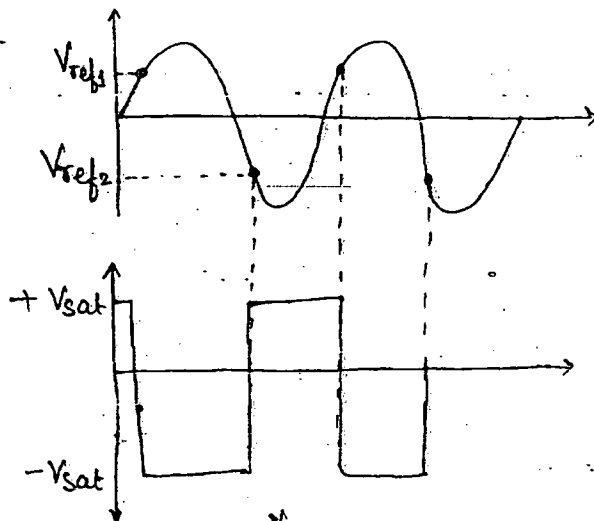
$$V_0 = (+) V_{sat}$$

$$V_{ref1} = + V_{sat} \times \frac{R_1}{R_1 + R_2}$$

$$V_0 = - V_{sat}$$

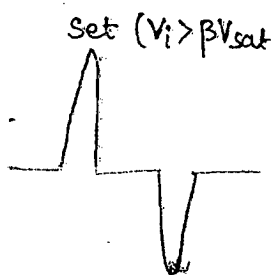
$$V_{ref2} = - V_{sat} \times \frac{R_1}{R_1 + R_2}$$

* Schmitt trigger creates V_{ref} itself.

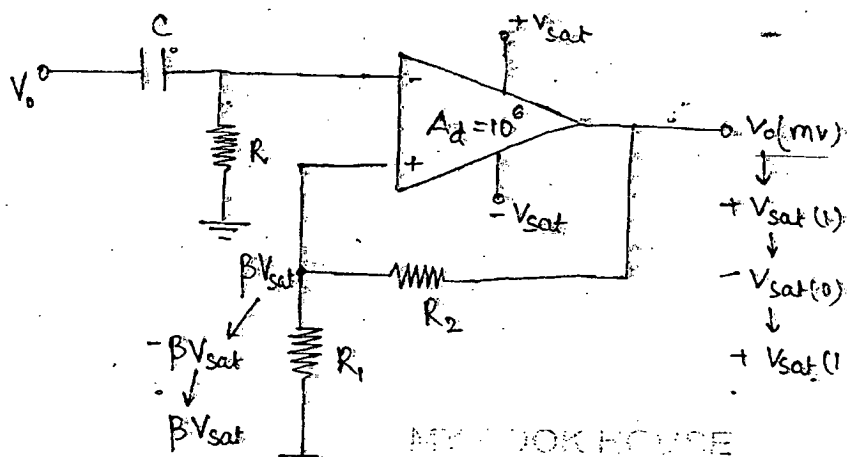


□ BISTABLE MULTIVIBRATOR.

(Flip Flop)

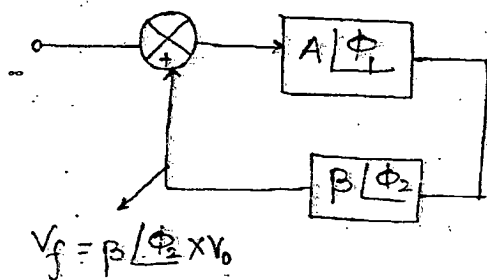


Reset ($V_i < -BV_{sat}$)



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□ OSCILLATORS.



$$V_o = A/\Phi_1 \{ V_i + V_o \beta / \Phi_2 \}$$

$$V_o = A/\Phi_1 V_i + A\beta / \Phi_1 + \Phi_2 \cdot V_o$$

$$V_o(1 - A\beta / \Phi_1 + \Phi_2) = A/\Phi_1 \cdot V_i$$

$$1 - A\beta / \Phi_1 + \Phi_2 = 0$$

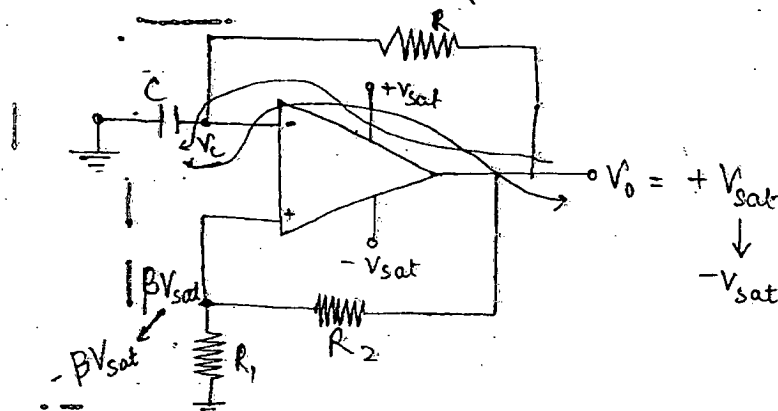
$$\frac{V_o}{V_i} = \frac{A/\Phi_1}{1 - A\beta / \Phi_1 + \Phi_2}$$

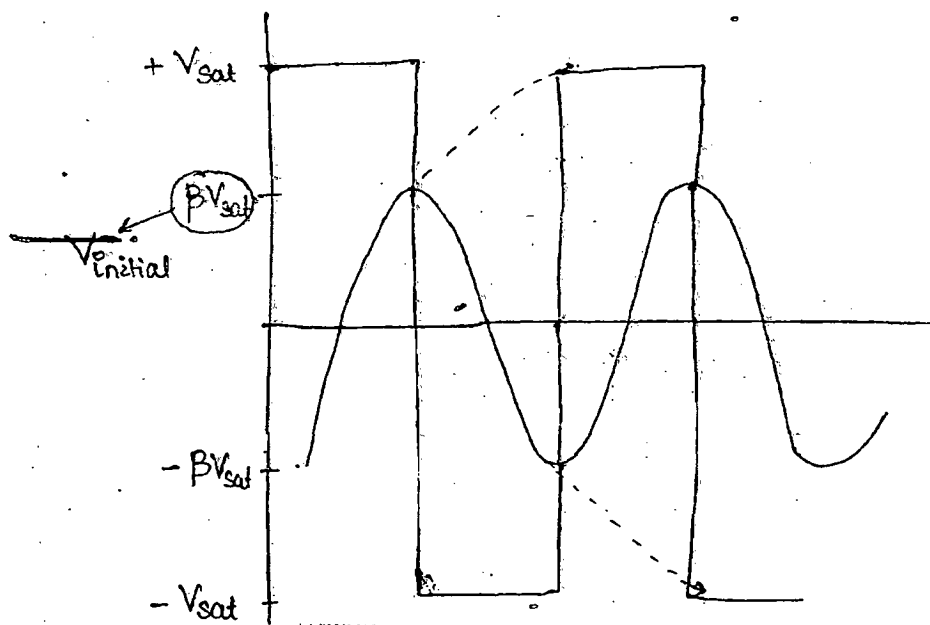
$$(1) A\beta = 1$$

$$(2) \Phi_1 + \Phi_2 = 0 (0\pi) \text{ multiple of } 2\pi$$

Barkhausen Criteria.

□ MONOSTABLE MULTIVIBRATOR.





$$V_c = V_{final} + [V_{initial} - V_{final}] e^{-t/RC}$$

$$\text{At } T = T_2$$

$$-BV_{sat} = -V_{sat} + (BV_{sat} + V_{sat}) e^{-T_2/RC}$$

$$(1-\beta)V_{sat} = (1+\beta)V_{sat} e^{-T_2/RC}$$

$$T_2 = RC \ln \left(\frac{1+\beta}{1-\beta} \right)$$

$$T_1 = RC \ln \left(\frac{1+\beta}{1-\beta} \right)$$

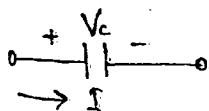
$$T = 2RC \ln \left(\frac{1+\beta}{1-\beta} \right)$$

$$\Rightarrow f = \frac{1}{2RC \ln \left(\frac{1+\beta}{1-\beta} \right)}$$

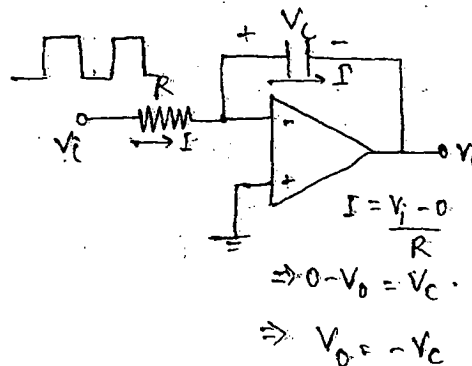
$$\text{where } \beta = \frac{R_1}{R_1 + R_2}$$

□ TRIANGULAR WAVE GENERATOR.

INTEGRATOR.



$$V_c = -\frac{1}{C} \int I dt$$



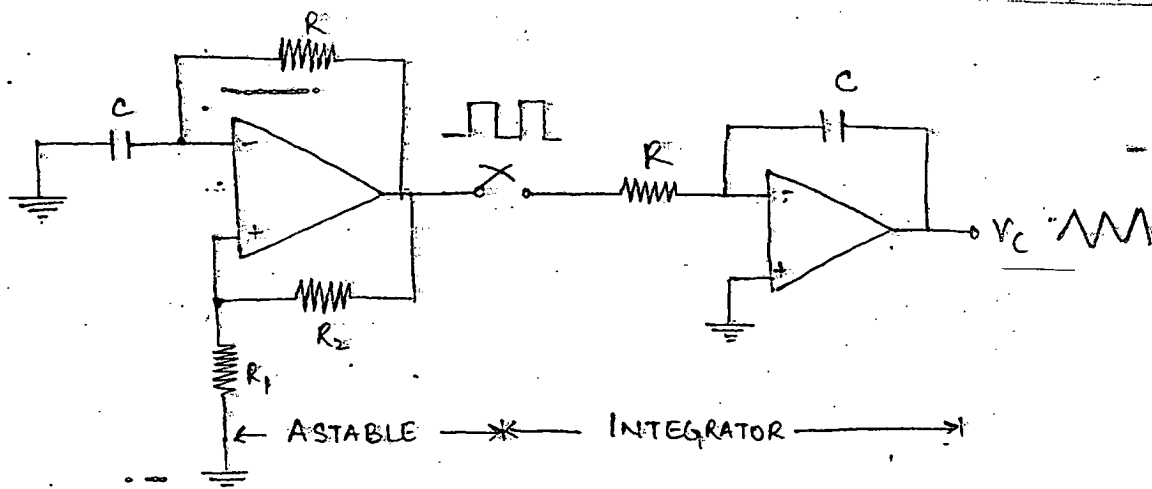
$$V_c = -\frac{1}{C} \int I dt$$

$$V_c = -\frac{1}{RC} \int V_i dt$$

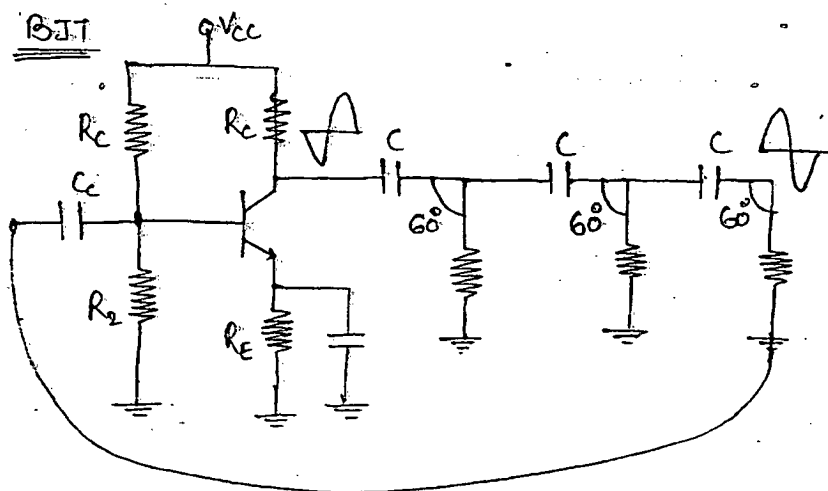


$$\Rightarrow 0 - V_c = V_c$$

$$\Rightarrow V_c = -V_c$$



□ RC PHASE SHIFT OSCILLATOR.



(1) $\phi + 3 \text{ sets of } RC = 360^\circ$
 $\downarrow 180^\circ \quad \downarrow 180^\circ$

(2) $f = \frac{1}{2\pi RC\sqrt{6+4K}}$

Where $K = R_c/R$

$K = 1$

$K \ll 1$

$f = \frac{1}{2\pi RC\sqrt{10}}$

$f = \frac{1}{2\pi RC\sqrt{6}}$

(3) $-h_{fe} \frac{R_c}{R} = -29 - 29K - 4K^2$
 $K \ll 1$

$A = -29$

$\beta = -\frac{1}{29} \quad \phi = 180^\circ$

(4) $h_{fe_{min}} = 44.5$

$h_{fe} = \frac{29}{K} + 29 + 4K$

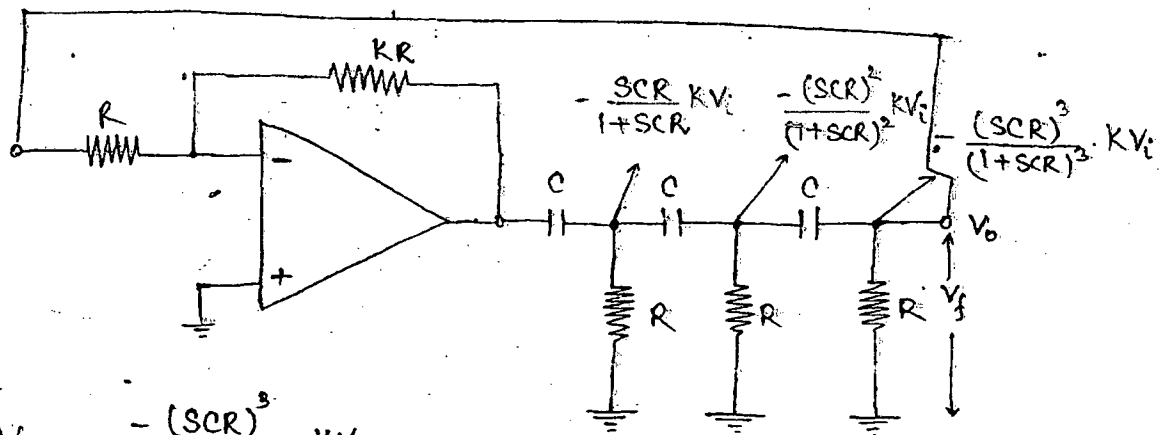
d.w.r. to K

$\frac{\partial h_{fe}}{\partial K} = -\frac{29}{K^2} + 4 = 0$

$K = 2.8$

$h_{fe} = 44.5$

OP-AMP



conv.

$$V_f = \frac{-(SCR)^3}{(1+SCR)^3} \cdot KV_i$$

$$\Rightarrow V_i = \frac{-(SCR)^3}{(1+SCR)^3} KV_i$$

$$\Rightarrow (1+SCR)^3 + (SCR)^3 K = 0$$

$$\Rightarrow 1 + 3S^2CR^2 + 3SCR + (SCR)^3(K+1) = 0$$

$$\Rightarrow 1 - 3\omega^2 C^2 R^2 + 3j\omega CR - j\omega^3 R^3 C^3 (K+1) = 0$$

Real part

$$\Rightarrow 1 - 3\omega^2 C^2 R^2 = 0$$

$$\omega_0 = \frac{1}{\sqrt{3}RC}$$

$$f_0 = \frac{1}{2\pi RC\sqrt{3}}$$

Imaginary part

$$3 - \omega_0^2 C^2 R^2 (K+1) = 0$$

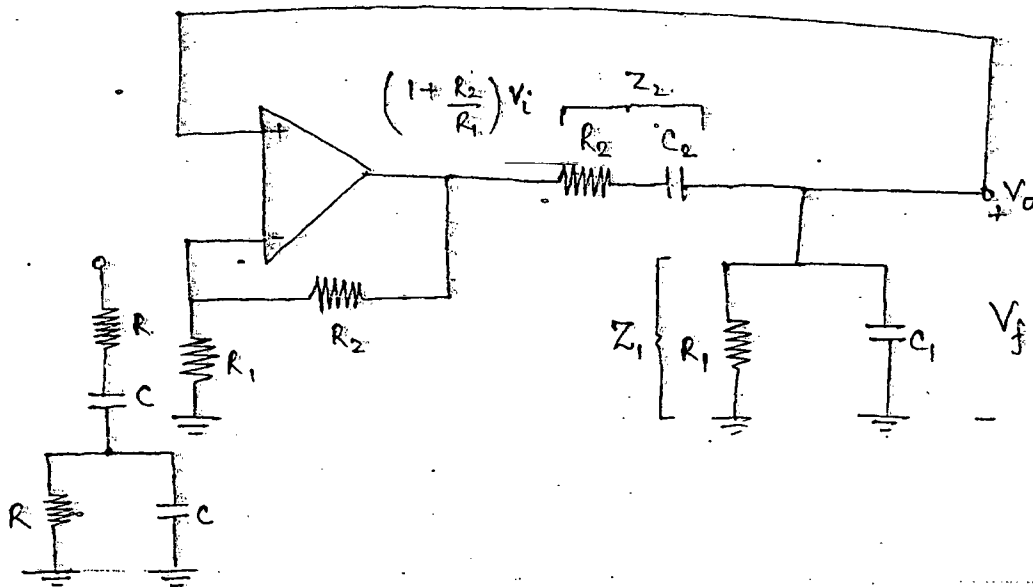
$$3 - \frac{1}{3R^2 C^2} \cdot C^2 R^2 (K+1) = 0$$

$$K+1=9 \Rightarrow \boxed{K=8}$$

CONCLUSION: DRAWBACKS:- 1. RC Phase Oscillator can be used for audio frequency (20Hz - 20KHz) because quality factor of RC N/W is multiples of 10.

2. This CRT can't be used for variable frequency operation because the β is depending on phase.

□ WEIN BRIDGE OSCILLATOR.



$$V_f = \left(1 + \frac{R_2}{R_1}\right) V_i \times \frac{Z_1}{Z_1 + Z_2}$$

$$= \left(1 + \frac{R_2}{R_1}\right) V_i \cdot \frac{1}{1 + Z_2 Y_1}$$

$$V_i = \frac{\left(1 + \frac{R_2}{R_1}\right) V_i}{1 + \left[R_2 + \frac{1}{sC_2}\right] \left[\frac{1}{R_1} + sC_1\right]}$$

$$V_i = \frac{\left(1 + \frac{R_2}{R_1}\right) V_i}{1 + \frac{R_2}{R_1} + \frac{C_1}{C_2} + \frac{1}{sC_2 R_1} + sC_1 R_2}$$

$$1 = \frac{\left(1 + \frac{R_2}{R_1}\right)}{1 + \frac{R_2}{R_1} + \frac{C_1}{C_2} + j\left(\omega C_1 R_2 - \frac{1}{\omega C_2 R_1}\right)}$$

Imaginary part = 0.

$$\Rightarrow \omega C_1 R_2 = \frac{1}{\omega C_2 R_1} \quad f_0 = \frac{1}{2\pi RC}$$

$$\Rightarrow \omega^2 = \frac{1}{R_1 R_2 C_1 C_2}$$

$$\omega_0 = \frac{1}{\sqrt{R_1 R_2 C_1 C_2}}$$

$$R_1 = R_2 = C$$

$$\omega_0 = \frac{1}{RC}$$

$$1 + \frac{R_2}{R_1} = 3$$

$$R_2 = 2R_1$$

Bridge balance

$$\phi = 0^\circ$$

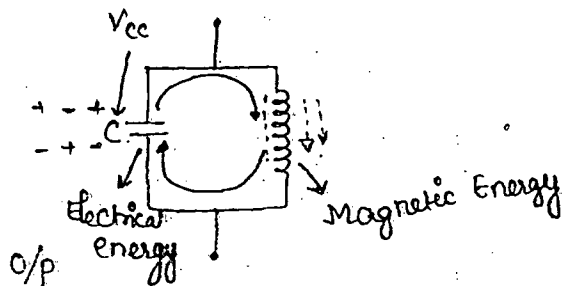
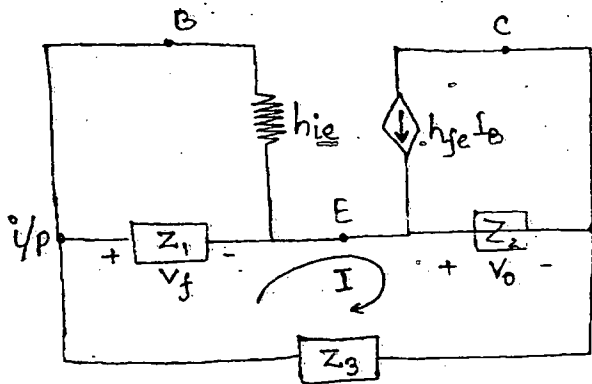
$$1 + \frac{R_2}{R_1} = 1 + \frac{R_2}{R_1} + \frac{C_1}{C_2}$$

$$A_v = 3$$

09/11/14.

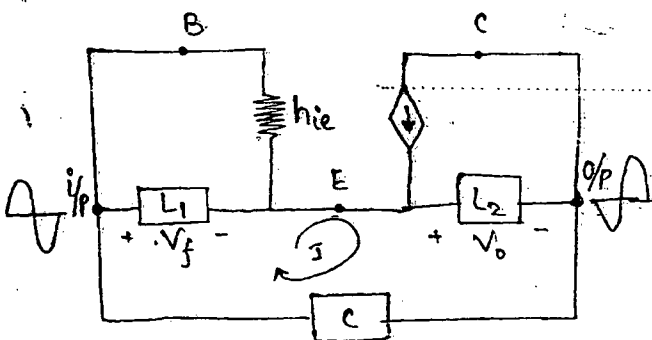
LC OSCILLATORS.

(Two port Network).



Quality factor, $Q = n \times 100$

Hartley Oscillator (MHZ)



$$f = \frac{1}{2\pi\sqrt{L_T C}}$$

$$L_T = L_1 + L_2$$

$$\beta = \frac{V_f}{V_o} = \frac{I \times L_1}{I \times L_2} = \frac{L_1}{L_2}$$

$$A = \left| \frac{L_2}{L_1} \right|$$

555 TIMER

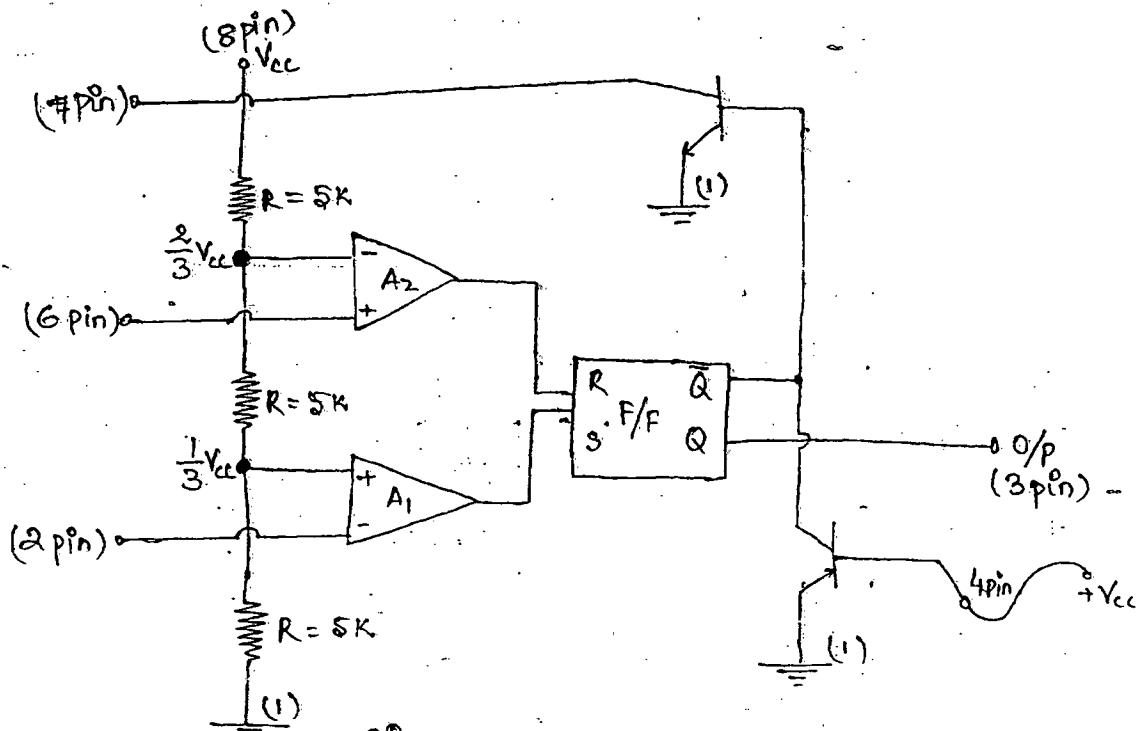
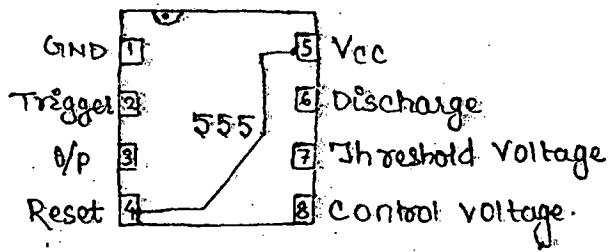
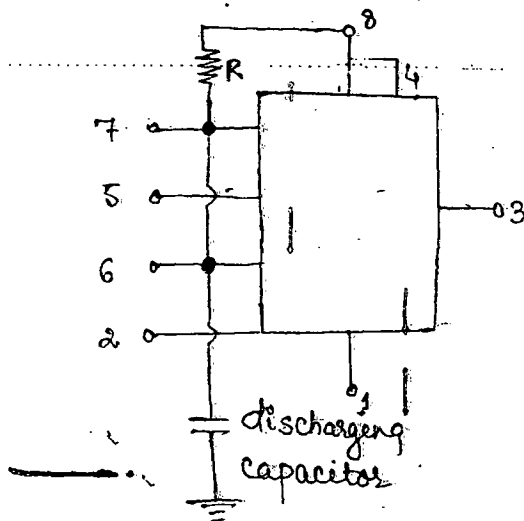
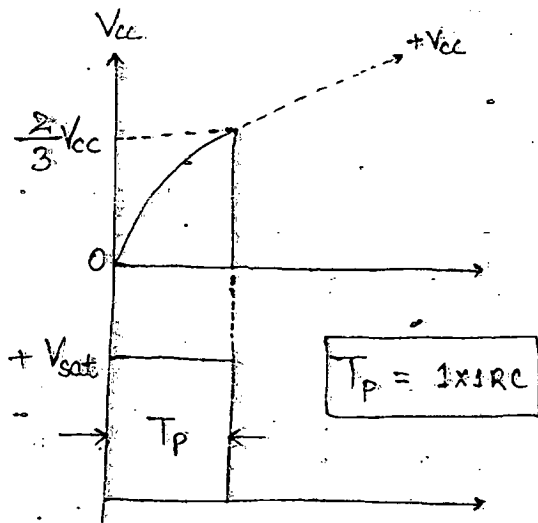


Fig:- Block Diagram.

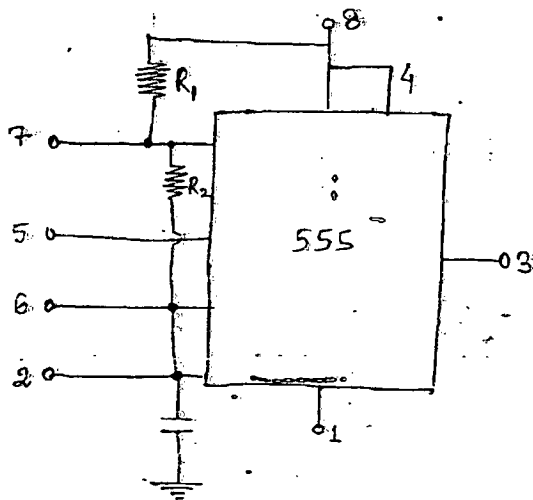
MONOSTABLE 555 TIMER :-



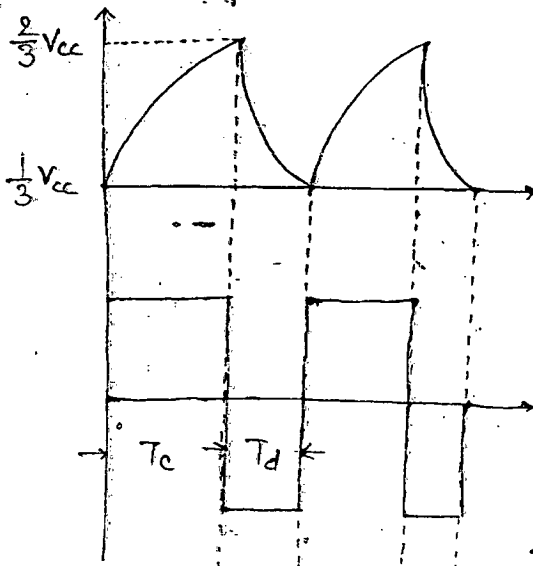
condition	Q	$\bar{Q}$	O/P	capacitor position
Standby	0	1	0	$V_c = 0$ (Stable)
Trigger $< \frac{1}{3} V_{cc}$	1	0	1	$V_c = \frac{2}{3} V_{cc}$ (Quasi)
$V_c > \frac{2}{3} V_{cc}$	0	1	0	$V_c = 0$ (Stable)



ASTABLE 555 TIMER.



Condition	Q	$\bar{Q}$	Q/P	Capacitor Position
Standby	0	1	0	$V_c = \frac{1}{3} V_{cc}$
$V_c < \frac{1}{3} V_{cc}$	1	0	1	$V_c = \frac{2}{3} V_{cc}$
$V_c > \frac{2}{3} V_{cc}$	0	1	0	$V_c = \frac{1}{3} V_{cc}$



$$T_c = 0.693 (R_1 + R_2) C$$

$$T_d = 0.693 R_2 C$$

$$T = T_c + T_d = 0.693 (R_1 + 2R_2) C$$

$$D.C = \frac{T_c}{T} = \frac{R_1 + R_2}{R_1 + 2R_2}$$

Duty cycle.

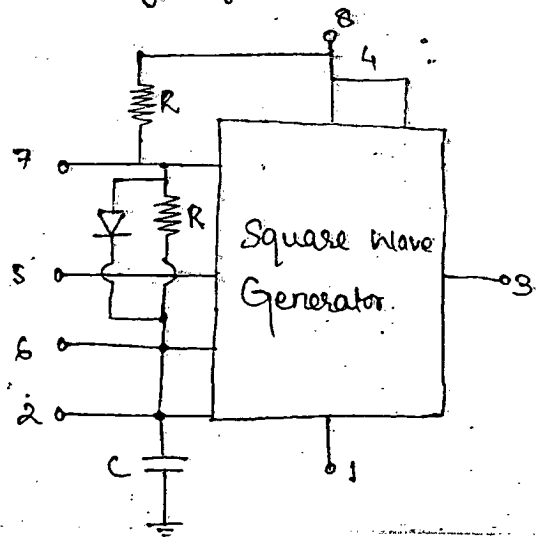
Question :- Design a Square wave generator using Astable multivibrator (555 timer) with a duty cycle 50%.

Solution :- $D.C = \frac{R_1 + R_2}{R_1 + 2R_2}$

Charging time, $T_c = 0.693 R_1 C$

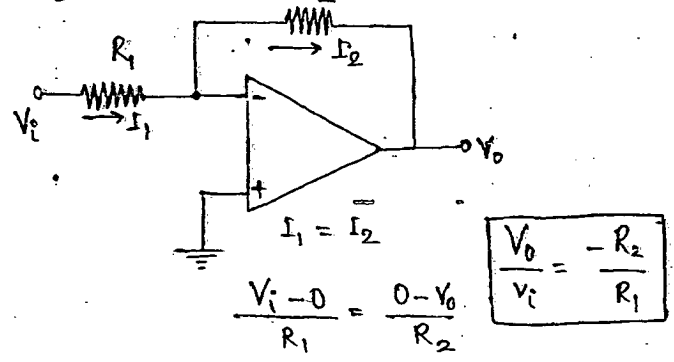
Discharging time, $T_d = 0.693 R_2 C$

$D.C = \frac{R}{R+R} = 50\%$

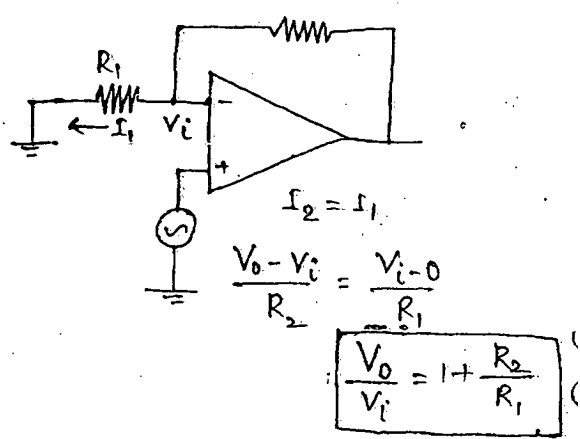


□ LINEAR APPLICATIONS.

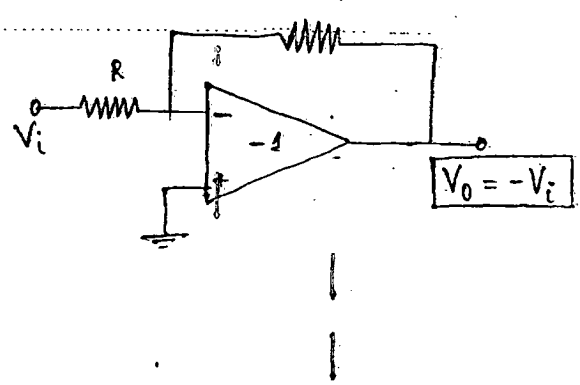
INVERTING AMPL^R



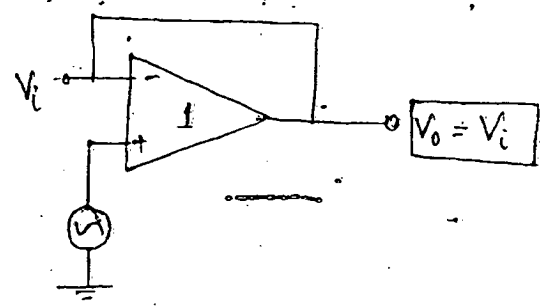
NON-INVERTING AMPL^R



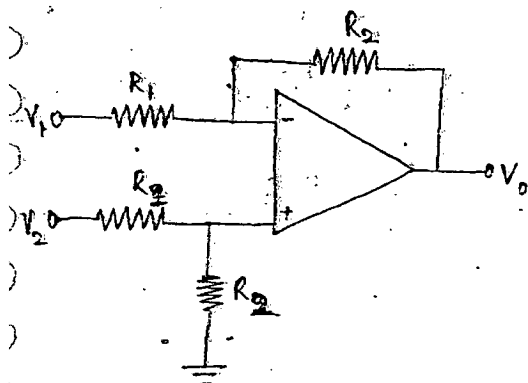
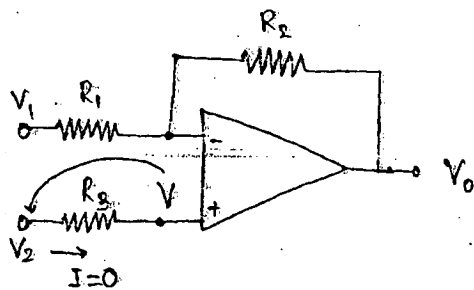
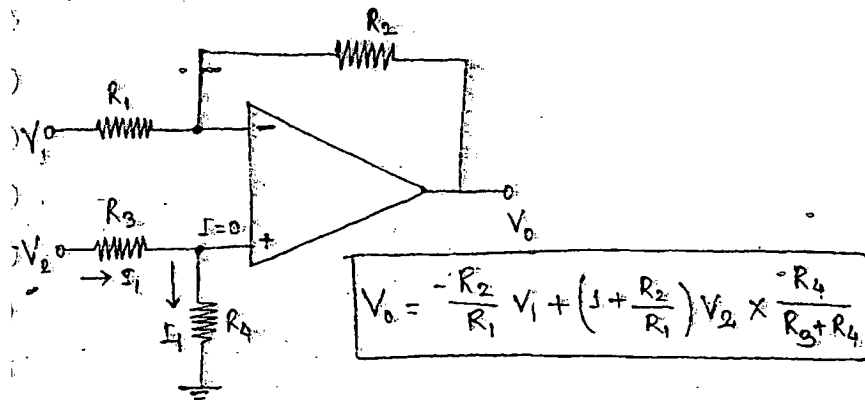
PHASE SHIFT



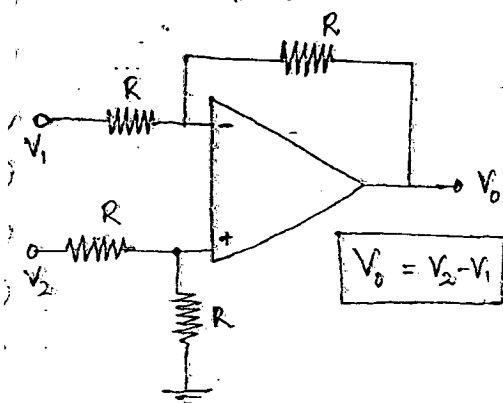
VOLTAGE FOLLOWER

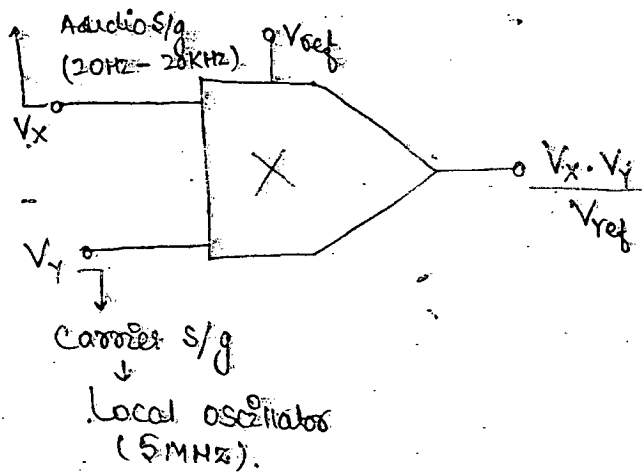
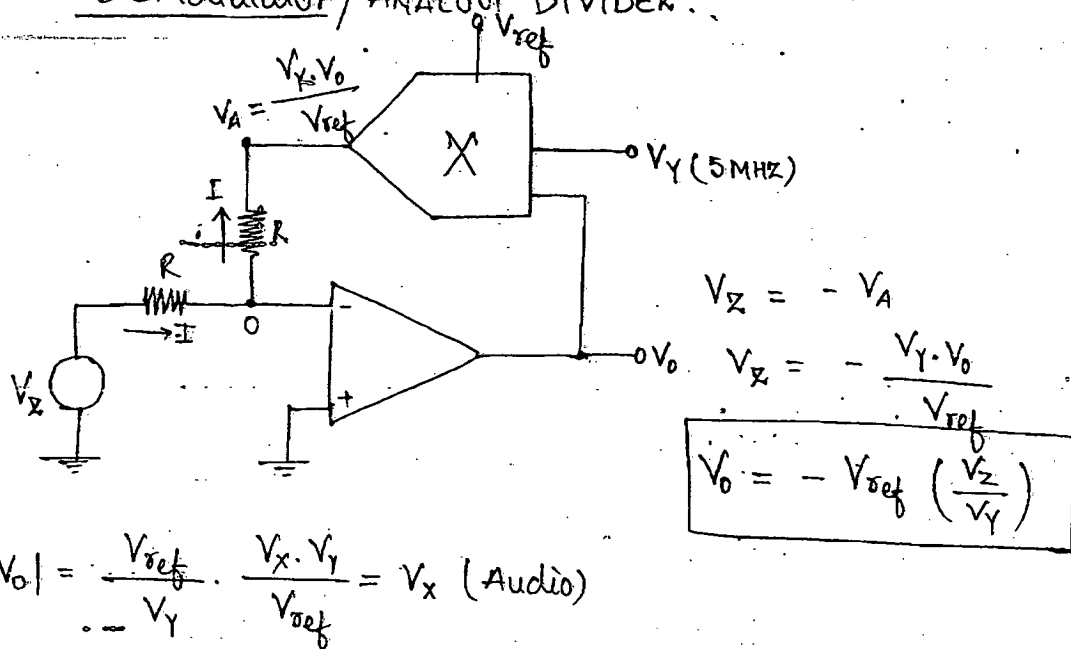


□ DIFFERENTIAL AMPLIFIER

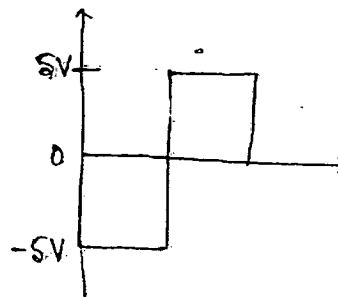
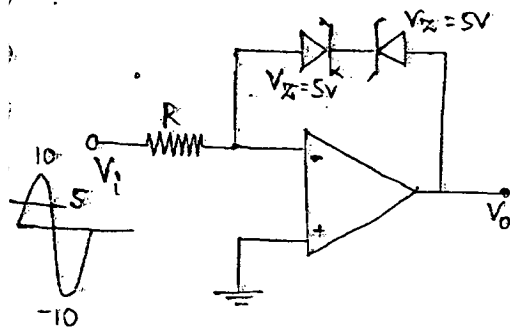


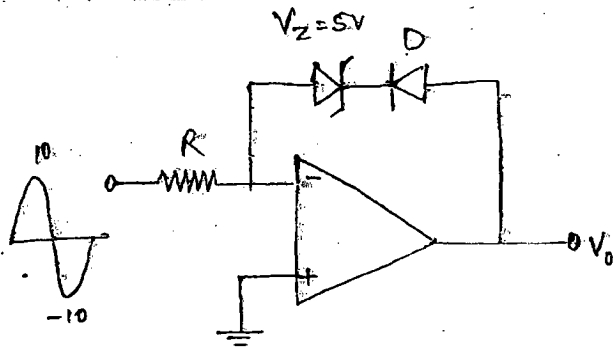
□ SUBTRACTOR



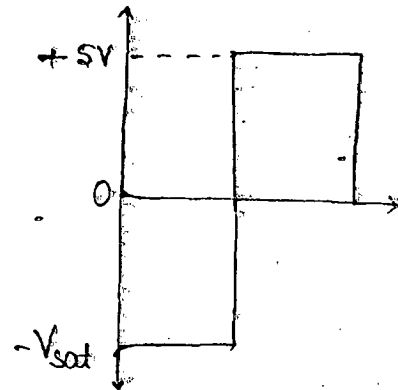
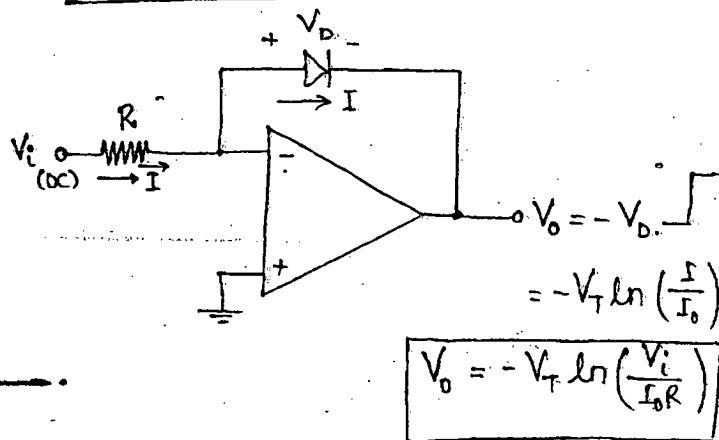
modulatorDemodulator / ANALOG DIVIDER

□ VOLTAGE LIMITERS

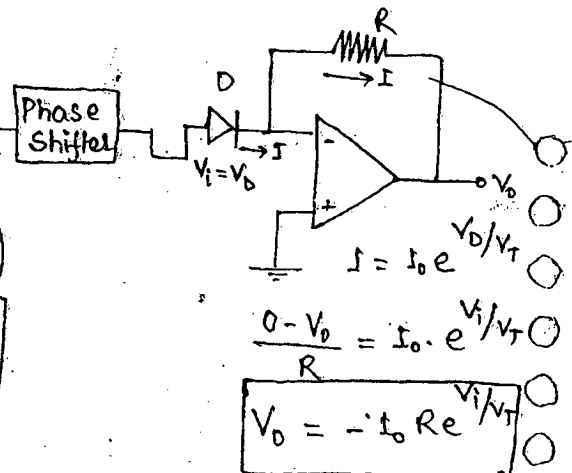




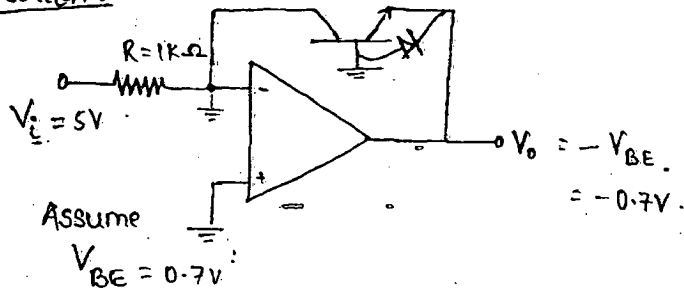
LOGARITHMIC AMPL^R



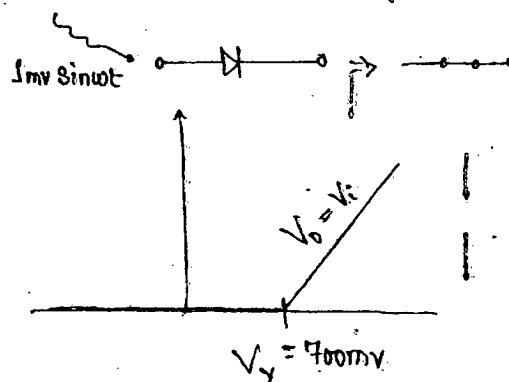
ANTILOGARITHMIC AMPL^R



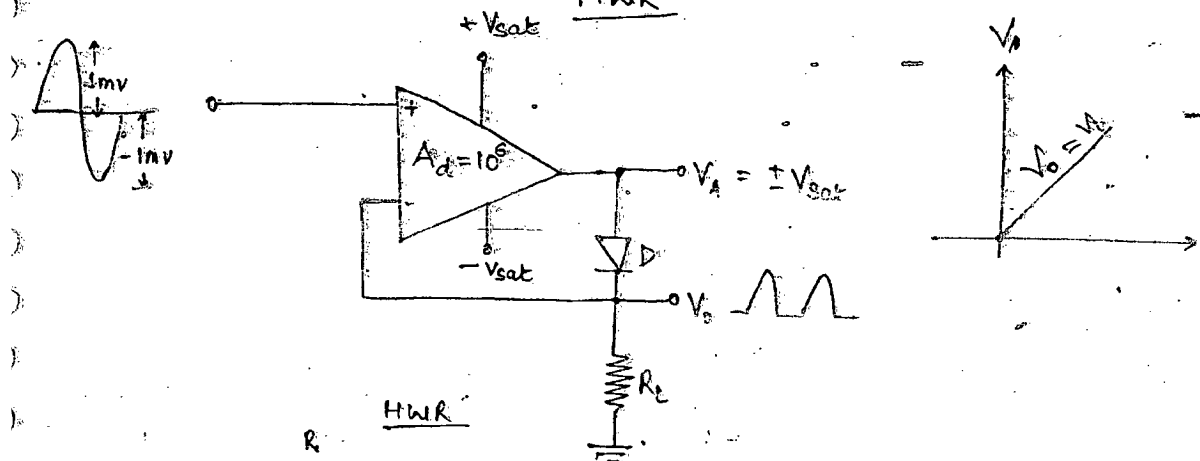
Question:-



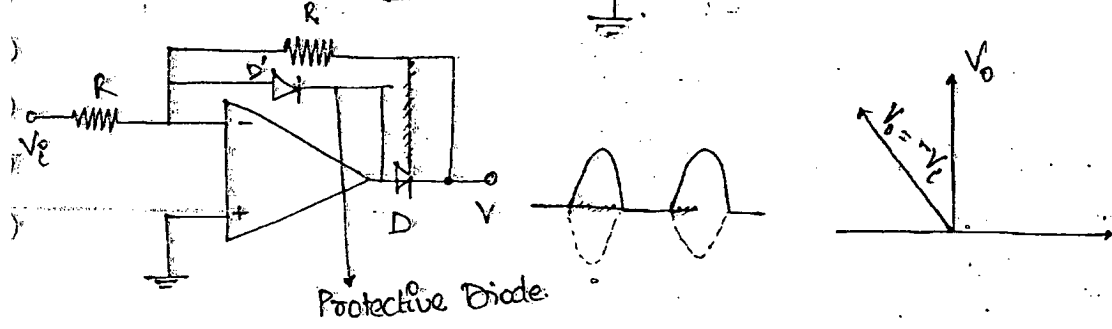
□ PRECISION RECTIFIER.



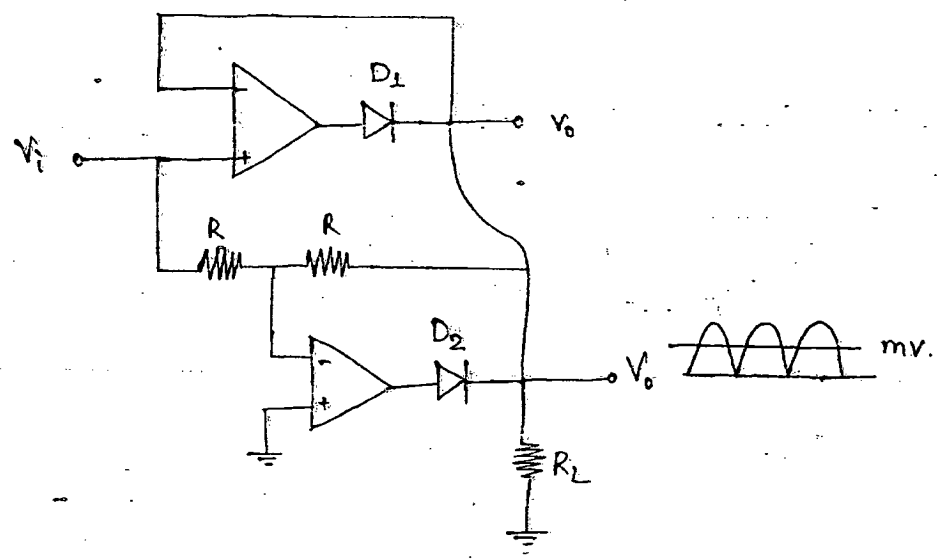
HWR



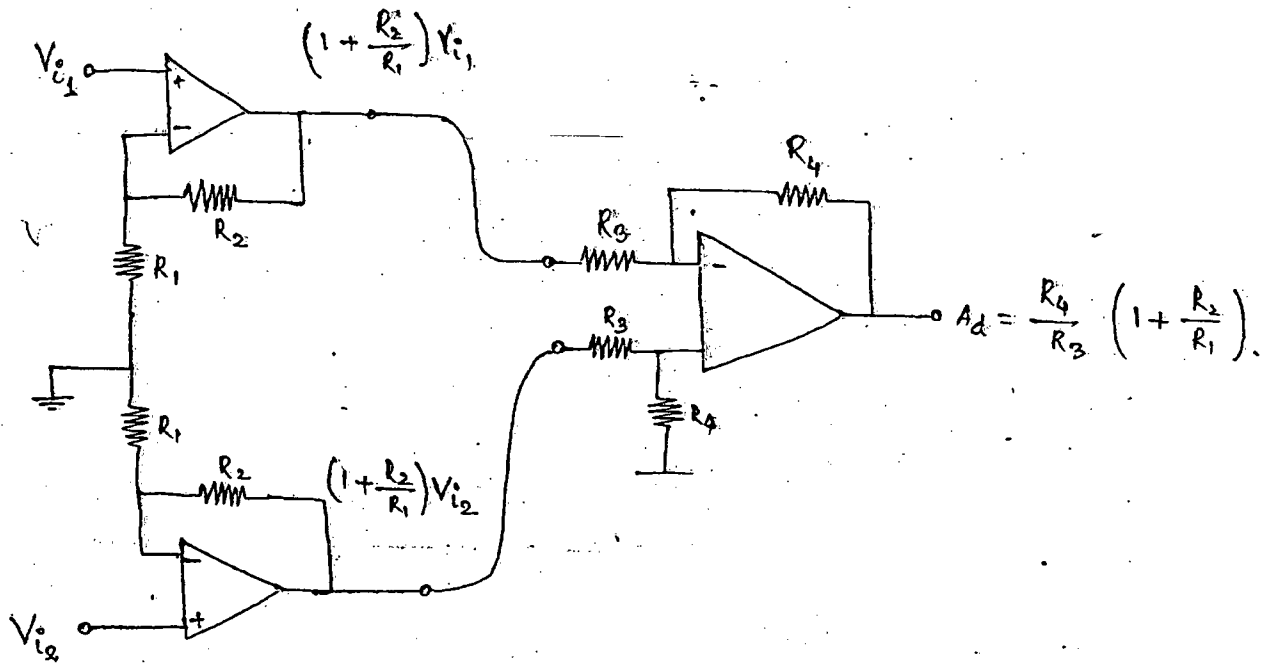
HWR



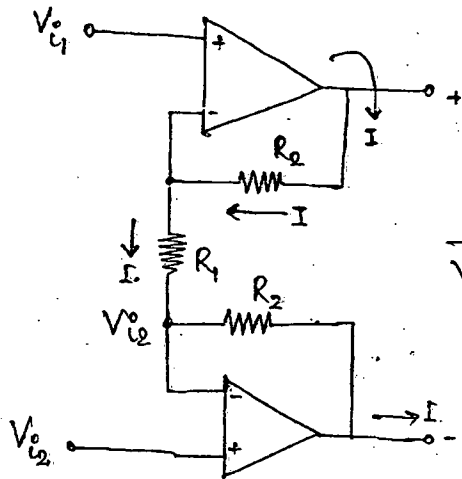
FWR



□ INSTRUMENTATIONAL AMPLIFIER.



MODIFIED CIRCUIT.

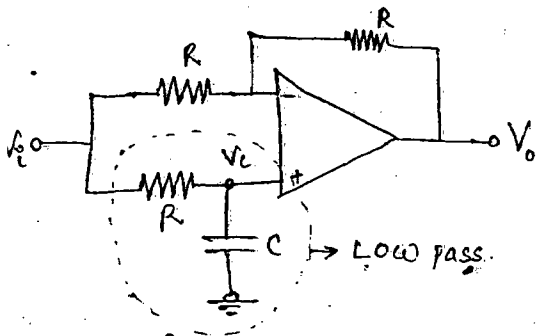


$$A_d = \frac{V_o}{V_{i1} - V_{i2}} = ?$$

$$\frac{V_o}{V_{i1} - V_{i2}} = \frac{I(R_1 + 2R_2)}{I R_1} = 1 + \frac{2R_2}{R_1}$$

Exp.

□ ALL PASS FILTER (ACTIVE FILTER)



$$\begin{aligned} V_o &= -\frac{R}{R} V_i + \left(1 + \frac{R}{R}\right) V_c \\ &= -V_i + 2V_i - \frac{jX_c}{R - jX_c} \\ &= V_i \left[-1 + \frac{2}{1 + j2\pi fRC} \right] \end{aligned}$$

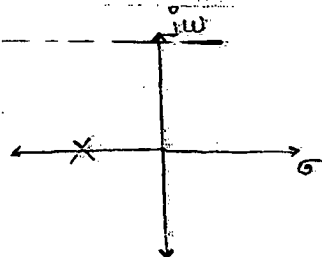
$$\frac{V_o}{V_i} = \frac{1 - j2\pi f RC}{1 + j2\pi f RC}$$

$$\left| \frac{V_o}{V_i} \right| = 1$$

$$\phi = -2 \tan^{-1} 2\pi f RC$$

$$-180^\circ \text{ to } 0^\circ$$

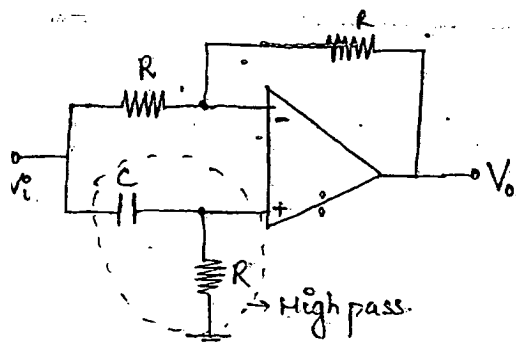
for 1st order.



For 2nd order

$$\frac{V_o}{V_i} = \frac{s^2 - as + b}{s^2 + as + b}$$

$$-360^\circ \text{ to } 0^\circ$$

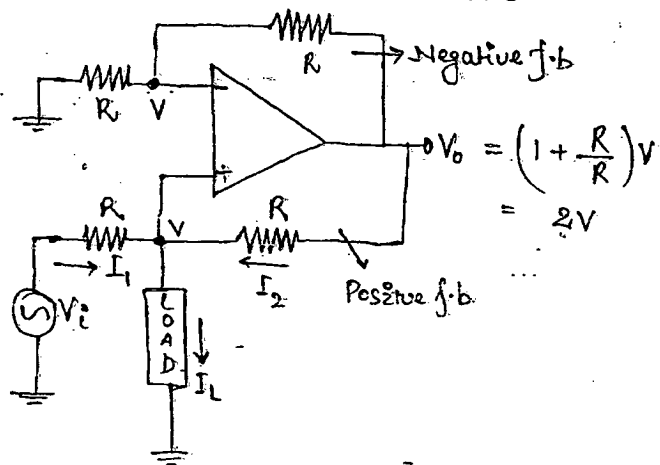


$$\phi = +2 \tan^{-1} 2\pi f RC$$

$$0^\circ \text{ to } 180^\circ \rightarrow 1^{\text{st}} \text{ order}$$

$$0 \text{ to } 360^\circ \rightarrow 2^{\text{nd}} \text{ order}$$

□ NEGATIVE AND POSITIVE FEEDBACK.



$$I_1 + I_2 = I_L$$

$$\frac{V_i - V}{R} + \frac{V_o - V}{R} = I_L$$

$$V_i + \underbrace{V_o}_{2V} - 2V = I_L R$$

$$\Rightarrow I_L = \frac{V_i}{R}$$