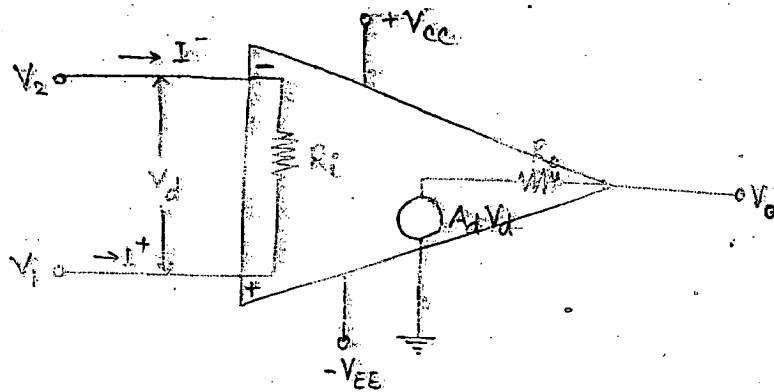


OP AMP



$$V_d = V_1 - V_2$$

$$R_i = \infty \text{ but } (1 \text{ M}\Omega)$$

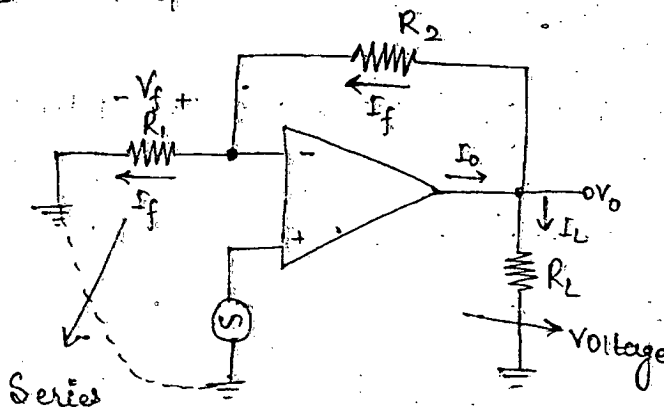
$$R_o = 0 \text{ but } (25 \Omega)$$

$$A_d = \text{gain} = \infty \text{ but } (10^6)$$

Ideal Theory :- conditions.

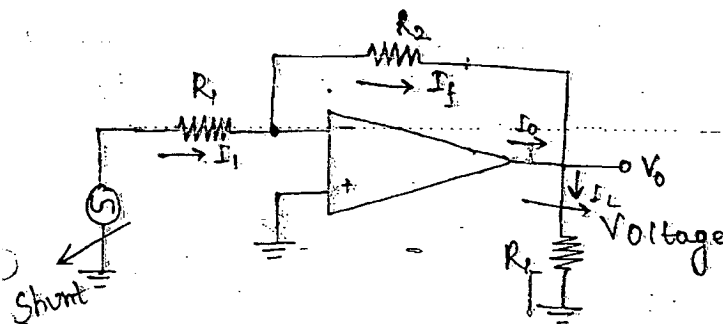
- 1) $R_i = \infty$ 2) $R_o = 0$ 3) $I_1^+ = 0 ; I_1^- = 0$ 4) $I_o \neq 0$ 5) $A_d = \infty$

VOLTAGE SERIES.



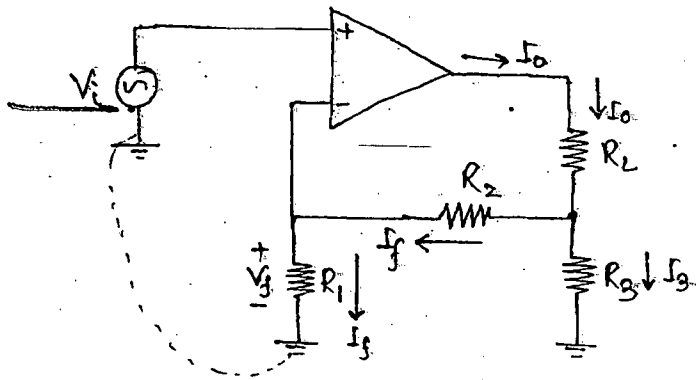
(+)ve is always
connected to voltage
source.
Non-Inverting.

VOLTAGE SHUNT

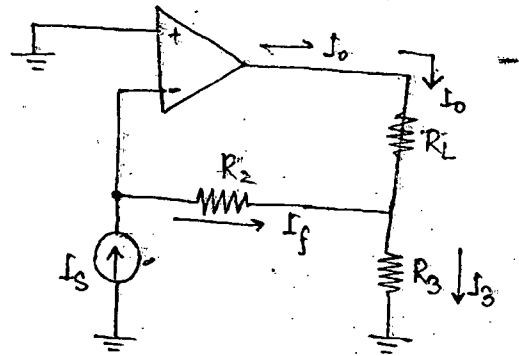


(-)ve is always connects
to voltage source
Inverting.

□ CURRENT SERIES.



□ CURRENT SHUNT.



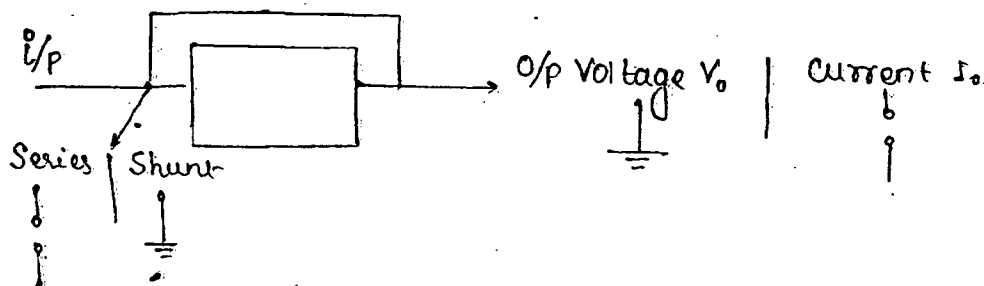
□ PROCEDURE FOR THE ANALYSIS OF NEGATIVE FEEDBACK AMPLIFIER

Case:- To get the i/p crt, modify the o/p .

1. For Voltage Series (or) voltage shunt make the o/p node ground.
2. For current Series (or) Voltage Series current shunt make the o/p loop open.

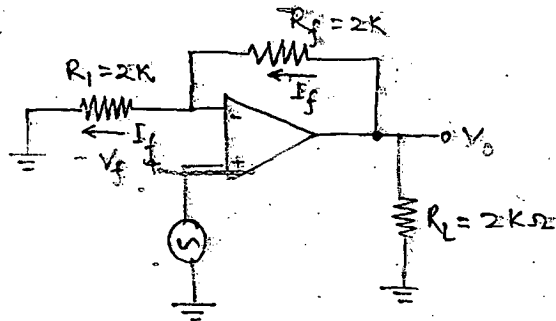
Case:- To get the o/p crt, modify the i/p .

1. For Voltage Series (or) current Series make the i/p loop open.
2. For Voltage Shunt (or) Current Shunt make the i/p node ground.



PROBLEM IN FEEDBACK AMPLIFIER.

Q1:-

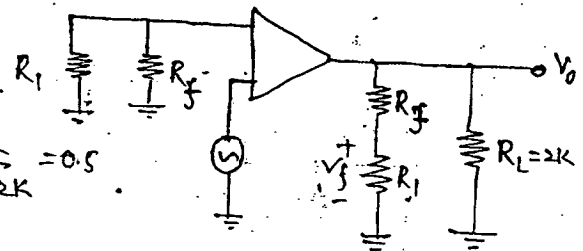


Solⁿ:- ∴ It is Voltage Series (S.C) (O.C)

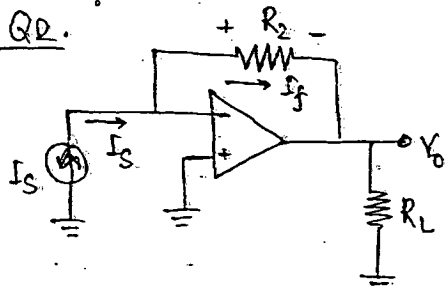
Final diagram

$$\therefore \beta = \frac{V_f}{V_o} = \frac{2K}{2K+2K} = 0.5$$

$$\Rightarrow A_f = 2.$$

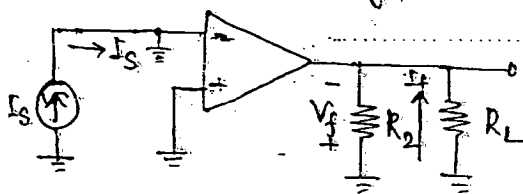


Q2.



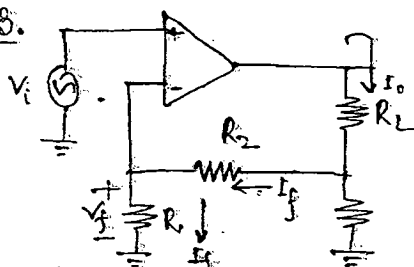
Calculate $\beta = \frac{V_f}{V_o} \Rightarrow \beta = \frac{I_f}{V_o}$

Solⁿ:- To get op. effect I_p is shorted grounded.
It is a voltage Shunt.

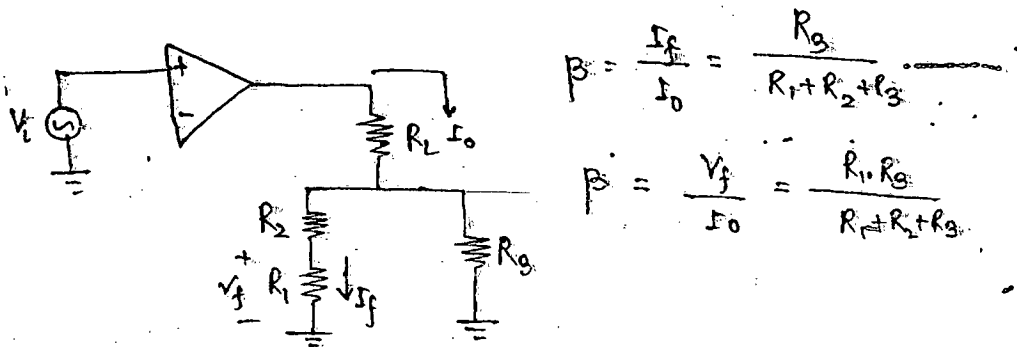


$$\beta = \frac{V_f}{V_o} = -1 \quad \left| \quad \beta = \frac{I_f}{V_o} = -\frac{1}{R_2} \right.$$

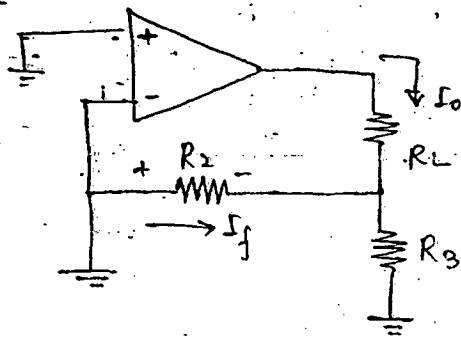
Q3.



Solⁿ:- $\therefore$ It is current series.

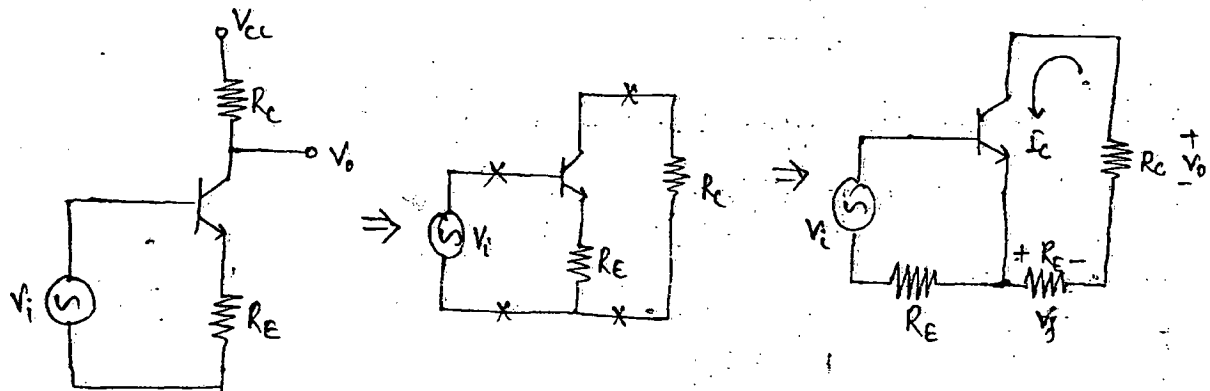


Que:-



$\therefore$ It is current shunt.

Que:- CE Unbypass Amplifier.

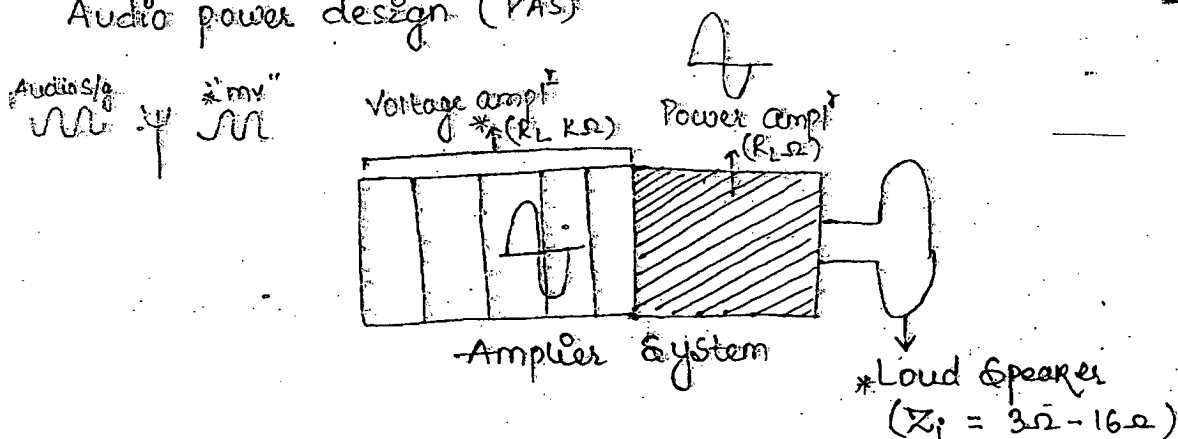


$$\beta = \frac{V_f}{V_o} = \frac{I_E R_E}{-I_E R_C} = -\frac{R_E}{R_C}$$

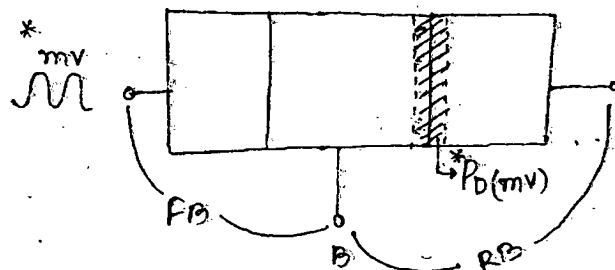
$$A_f = \frac{1}{\beta} = -\frac{R_C}{R_E}$$

□ POWER AMPLIFIERS

Audio power design (PAS)



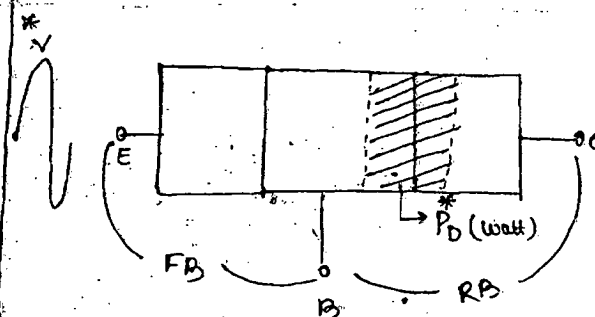
Voltage transistor



*Base width is narrower

$$*\beta \uparrow = \frac{I_C}{I_B \downarrow} \quad (50 \text{ to } 150)$$

Power Transistor



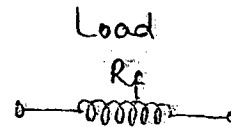
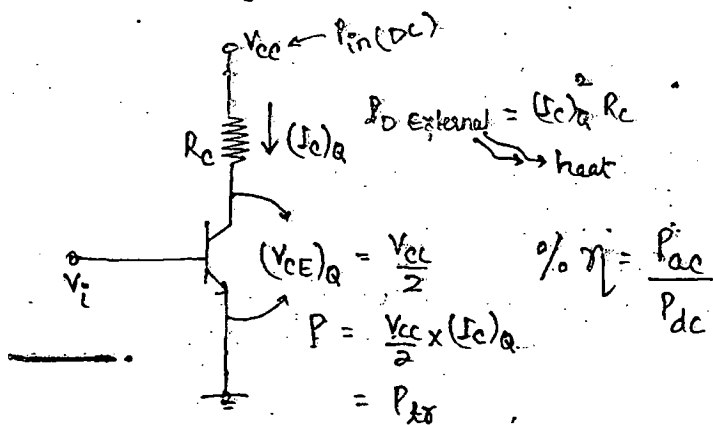
*Base width is wider

$$*\beta \downarrow = \frac{I_C}{I_B \uparrow} \quad (20 \text{ to } 50)$$

Parameter	Vol. Ampl ^r	Power Ampl ^r
1. β	More	Less
2. R_C	k Ω	Ω
3. V_{in}	mv	V
4. P_o	mW	W
5. I_C	Less	More

Question Why Voltage ampl^r design can't be used as power Amplifier.

Voltage Amplifier

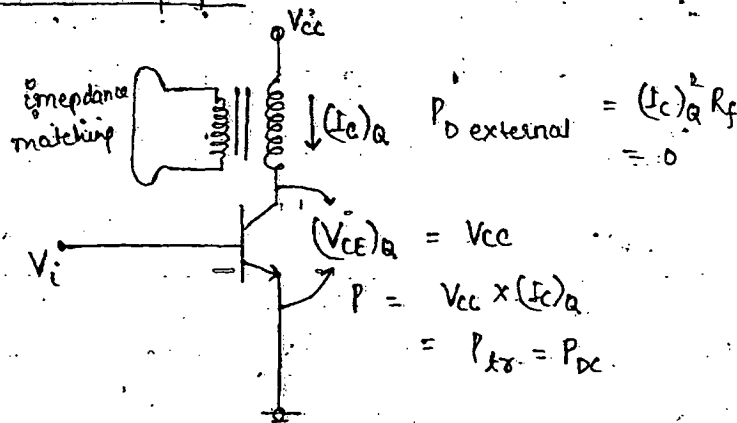


$$R_f \propto f$$

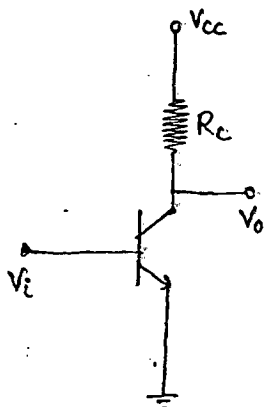
$$f = 0 \text{ (dc)} \quad R_f = 0 \text{ (s.c)}$$

$$f = \infty \text{ (ac)} \quad R_f = \infty \text{ (o.c)}$$

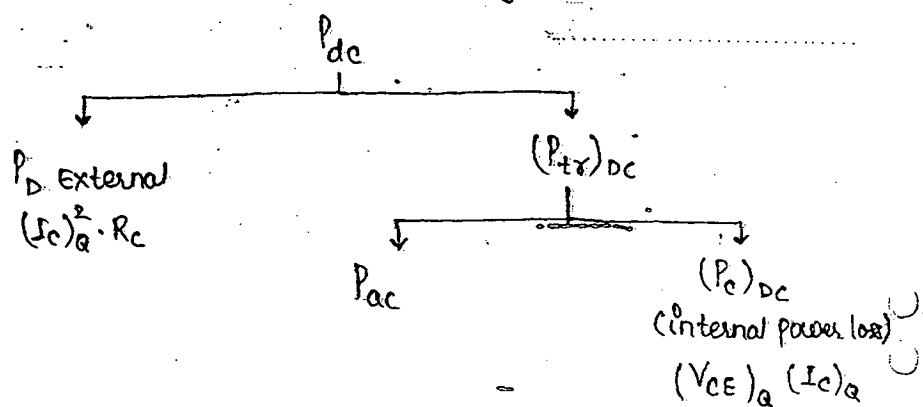
Power Amplifier



POWER AMPLIFIER DESIGN



Power diagram



$$P_{DC} = P_{ac} + P_D$$

$$P_D \text{ internal} + P_D \text{ external}$$

$\downarrow 50\%$ $\downarrow 50\%$

Suppose $P_D = 0$

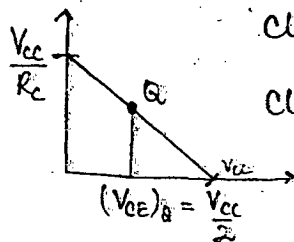
$$\% \eta = \frac{P_{ac}}{P_{dc}} = 100\%$$

DEFINITION OF POWER AMPLIFIER :- It is a device which convert dc power into ac power & whose action is controlled by ac input signal.

□ POWER AMPLIFIER CLASSES.

→ AUDIO POWER DESIGN.

1. CLASS A

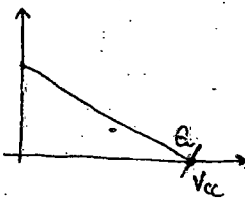


Class A Series fed power amplifier $\rightarrow \eta = 25\%$

Class A Transformer coupled $\rightarrow \eta = 50\%$

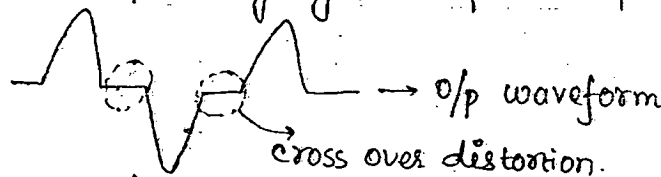
CLASS A

2. CLASS B

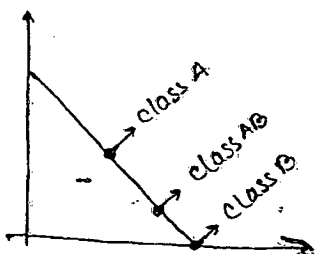


Class B push pull power amplifier

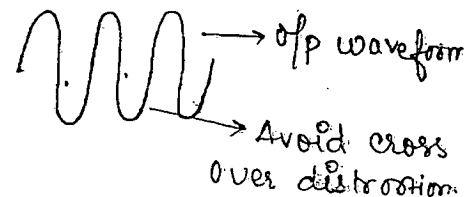
Class B complimentary symmetric power amplifier $\left\{ \eta = 78.5\% \right.$



3. CLASS AB

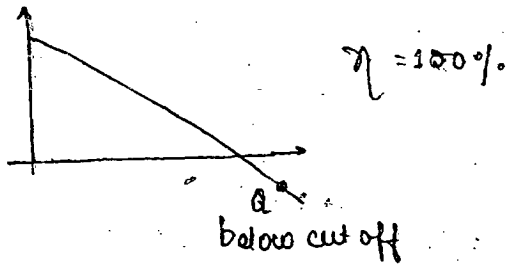


$\eta = 50\% \text{ to } 78.5\%$



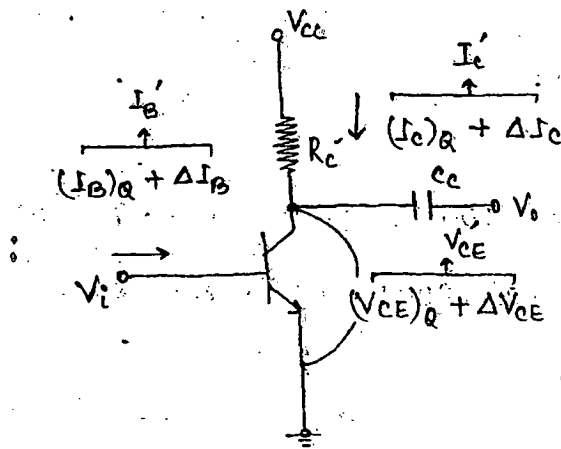
→ RADIO POWER DESIGN.

1. CLASS C

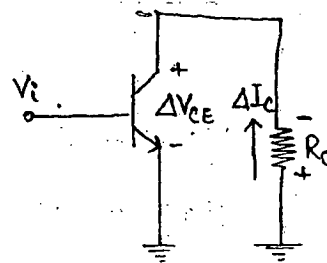


AC LOADLINE

□ AC LOAD LINE



ac model



— KVL at o/p.

$$\Delta I_c R_c + \Delta V_{ce} = 0$$

$$\Delta I_c = -\frac{\Delta V_{ce}}{R_c} \quad (1)$$

$$I_c' = (I_c)_Q + \Delta I_c \quad (2)$$

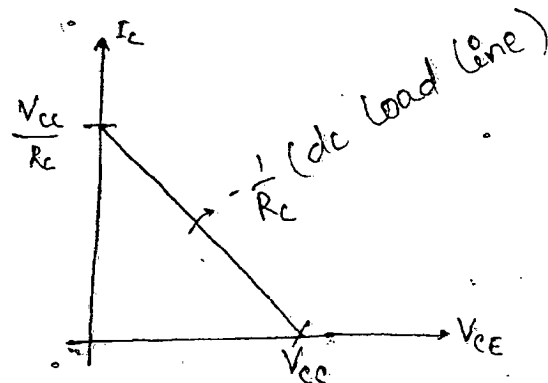
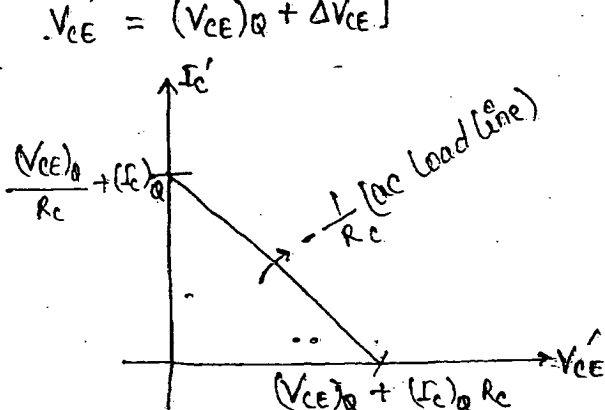
$$V_{ce}' = (V_{ce})_Q + \Delta V_{ce}$$

Putting eqⁿ (2) in (1) we get

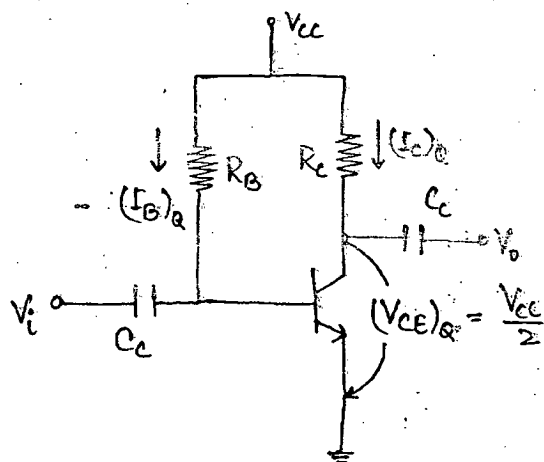
$$I_c' - (I_c)_Q = -\frac{V_{ce}'}{R_c} + \frac{(V_{ce})_Q}{R_c}$$

$$I_c' = -\frac{1}{R_c} \cdot V_{ce}' + \left\{ \frac{(V_{ce})_Q}{R_c} + (I_c)_Q \right\}$$

$$Y = m X + C$$



CLASS A SERIES FED POWER AMPLIFIER (R_c)



DC Power $\frac{W}{p}$

$$P_{DC} = V_{CC} \cdot (I_C)_Q$$

DC condition

$$(I_E)_Q = \frac{V_{CC} - V_{BE}}{R_B}$$

$$(I_C)_Q = \beta (I_B)_Q$$

$$(V_{CE})_Q = V_{CC} - (I_C)_Q R_c$$

AC Power $\frac{W}{p}$

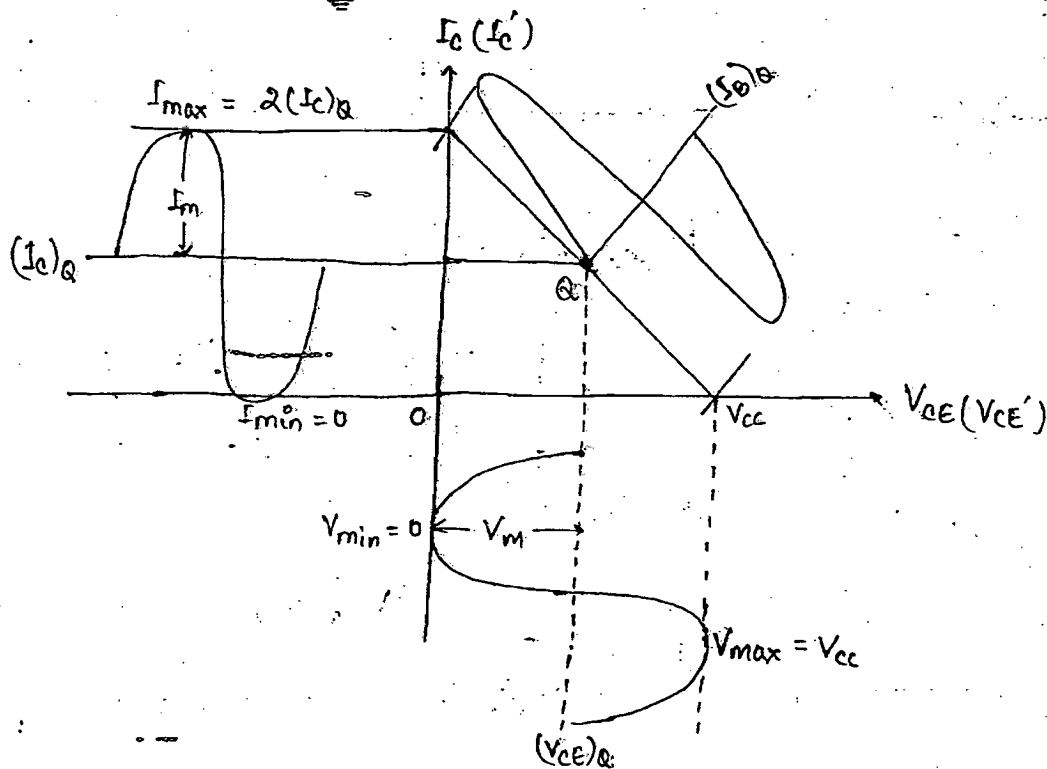
$$P_{AC} = V_{rms} \times I_{rms}$$

$$= \frac{V_m}{\sqrt{2}} \times \frac{I_m}{\sqrt{2}}$$

$$P_{AC} = \frac{V_m \cdot I_m}{2}$$

$$V_m = \frac{V_{max} - V_{min}}{2}$$

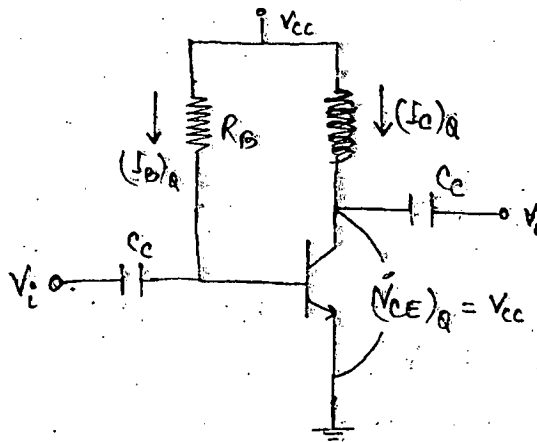
$$I_m = \frac{I_{max} - I_{min}}{2}$$



$$\% \eta = \frac{P_{AC}}{P_{DC}} = \frac{\frac{1}{2} (V_{max} - V_{min}) (I_{max} - I_{min})}{V_{CC} (I_C)_Q}$$

$$= \frac{\frac{1}{2} (V_{CC} - 0) (2(I_C)_Q - 0)}{V_{CC} (I_C)_Q} = \frac{1}{4} = 25\%$$

CLASS A TRANSFORMER COUPLED POWER AMPLIFIER



DC power $\frac{W}{p}$

$$P_{DC} = V_{CC} (I_C)_Q$$

DC condition

$$(I_B)_Q = \frac{V_{CC} - V_{BE}}{R_B}$$

$$(I_C)_Q = \beta (I_B)_Q$$

$$(V_{CE})_Q = V_{CC}$$

ac power $\frac{W}{p}$

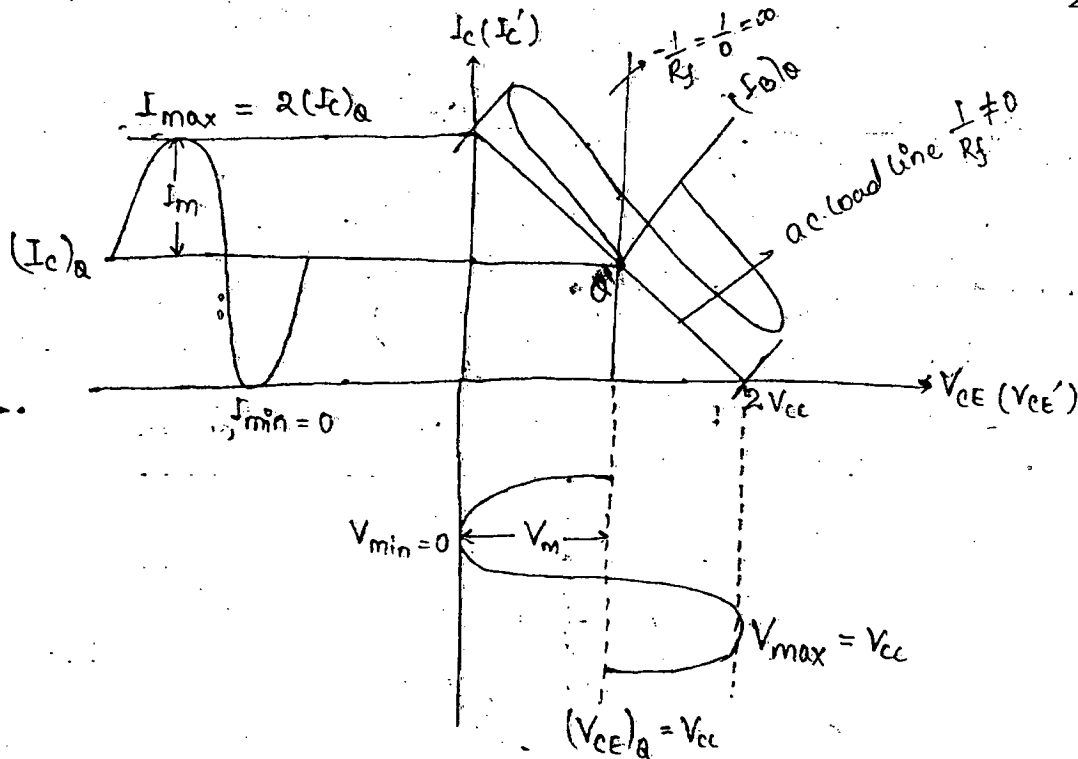
$$P_{ac} = V_{rms} \times I_{rms}$$

$$= \frac{V_m}{\sqrt{2}} \times \frac{I_m}{\sqrt{2}}$$

$$= \frac{V_m \cdot I_m}{2}$$

$$V_m = \frac{V_{max} - V_{min}}{2}$$

$$I_m = \frac{I_{max} - I_{min}}{2}$$



$$\% \eta = \frac{P_{ac}}{P_{dc}} = \frac{\frac{1}{2} (V_{max} - V_{min}) (I_{max} - I_{min})}{V_{CC} \cdot (I_C)_Q}$$

$$= \frac{1}{8} \frac{(2V_{CC} - 0) (2(I_C)_Q - 0)}{V_{CC} (I_C)_Q} = \frac{1}{2} = 50\%$$

□ POWER DISSIPATION IN CLASS A

$$P_D = P_{DC} - P_{ac}$$

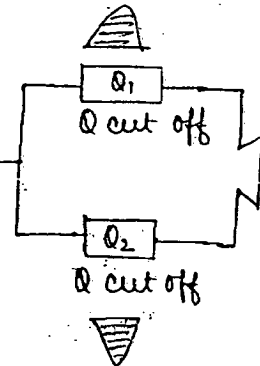
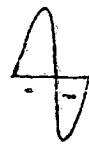
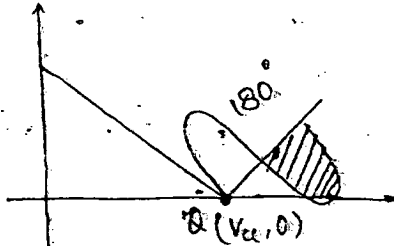
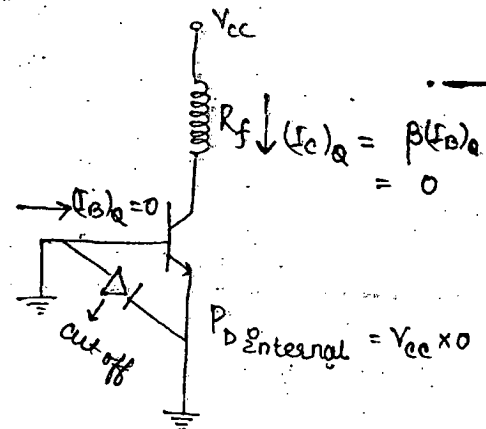
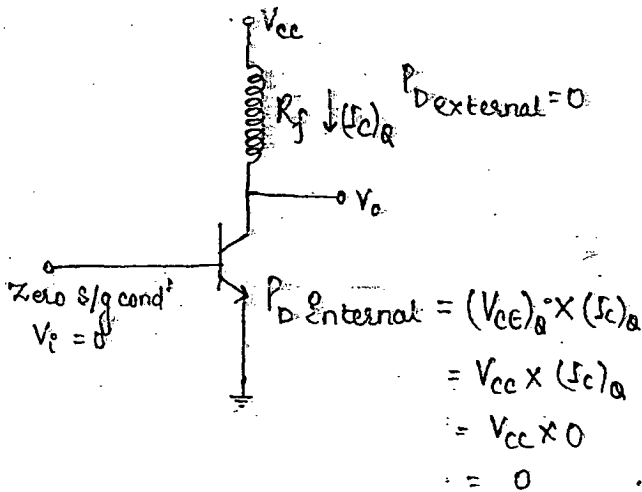
$$P_D = V_{CC} (I_C)_Q - \frac{V_m \cdot I_m}{2}$$

at zero s/g condition (DC)

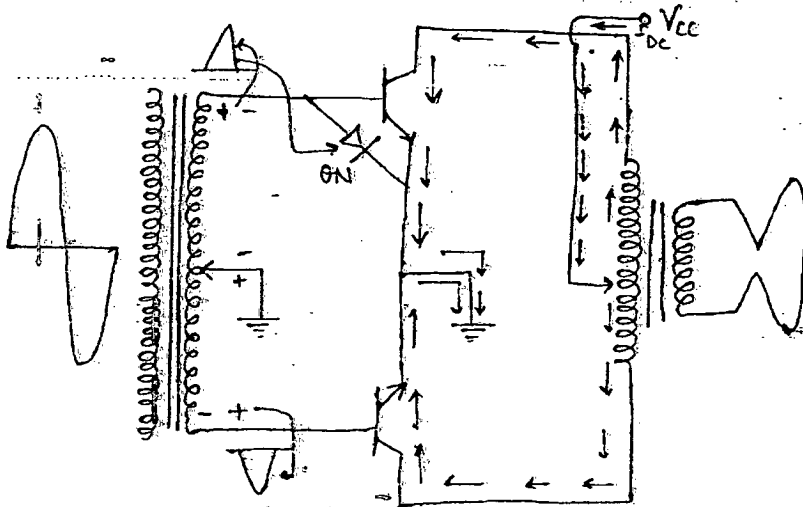
$$V_m = 0 ; I_m = 0$$

$$P_{DC} = V_{CC} (I_C)_Q = P_{DC} (\text{max}^m \text{ power loss})$$

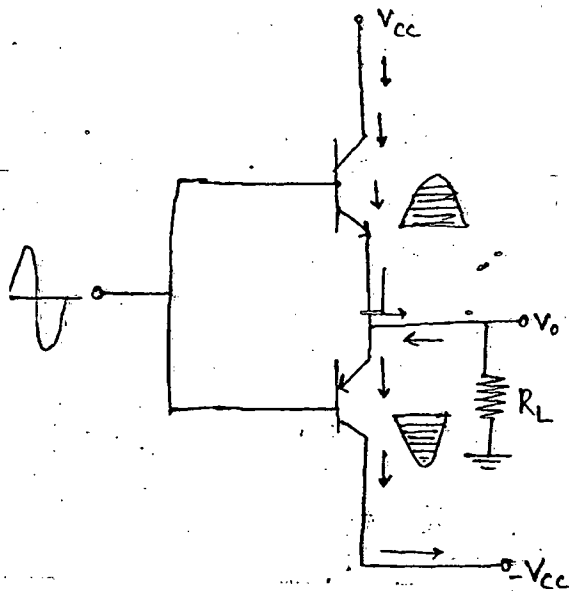
□ CLASS B



□ CLASS B PUSH PULL AMPLIFIER



□ CLASS B COMPLEMENTARY SYMMETRY POWER AMPLIFIER.



cc as impedance matching

□ POWER DISSIPATION.

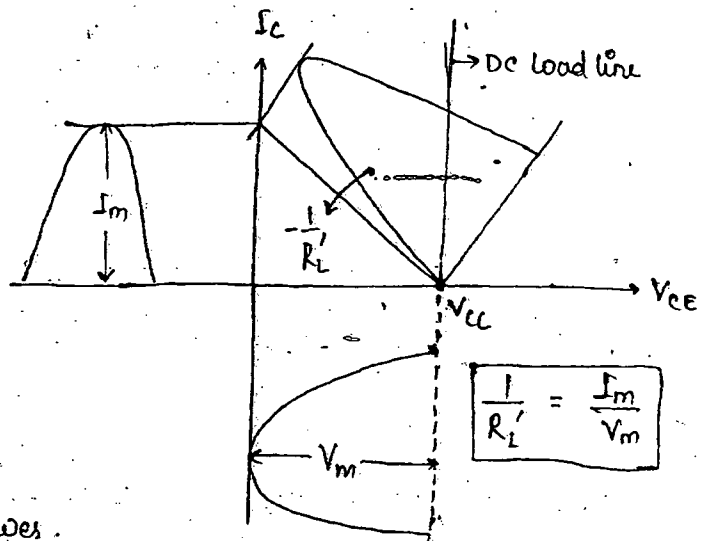
$$P_{DC} = V_{CC} \times I_{DC}$$

$$= 2V_{CC} \times \frac{I_m}{\pi}$$

$$P_{ac} = \frac{V_m I_m}{2}$$

$$\% \eta = \frac{P_{ac}}{P_{dc}} = \frac{\frac{V_m I_m}{2}}{2V_{CC} \cdot \frac{I_m}{\pi}}$$

$$V_m = V_{CC} \rightarrow \text{max}^m \text{ ac power.}$$



$$\% \eta = \frac{V_{CC} \cdot \frac{I_m}{\pi}}{2V_{CC} \cdot \frac{I_m}{\pi}} = \frac{\pi}{4} = 0.785 = 78.5\%$$

□ POWER DISSIPATION IN CLASS B.

$$P_{Dc} \quad P_D = P_{DC} - P_{ac}$$

$$= 2V_{CC} \cdot \frac{I_m}{\pi} - \frac{V_m I_m}{2}$$

At zero signal conditions (DC)

$$V_m = 0 ; I_m = 0$$

$$P_D = 0$$

Ac
Max^m power dissipation.

$$P_D = 2V_{CC} \frac{I_m}{\pi} - \frac{V_m I_m}{2}$$

$$\boxed{\frac{1}{R_L'} = \frac{I_m}{V_m}} \rightarrow \text{ac load line}$$

$$P_D = 2V_{CC} \cdot \frac{V_m}{\pi R_L'} - \frac{V_m^2}{2R_L'}$$

d.w.r.t V_m .

$$\frac{\partial P_D}{\partial V_m} = \frac{2V_{CC} \cdot 1}{\pi R_L'} - \frac{2V_m}{2R_L'} = 0$$

$$\Rightarrow \boxed{V_m = \frac{2V_{CC}}{\pi}} \rightarrow \text{max}^m \text{ power dissipation.}$$

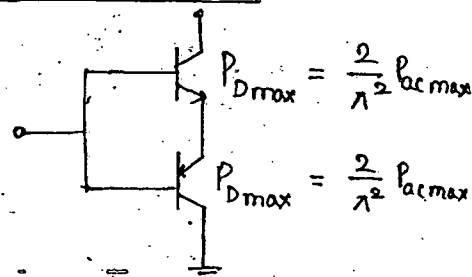
$$\boxed{V_m = V_{CC}} \rightarrow \text{Max}^m \text{ ac power.}$$

$$P_{Dmax} = \frac{2V_{CC}}{\pi R_L'} \cdot \frac{2V_{CC}}{\pi} - \frac{4V_{CC}^2}{\pi^2} \cdot \frac{1}{2R_L'}$$

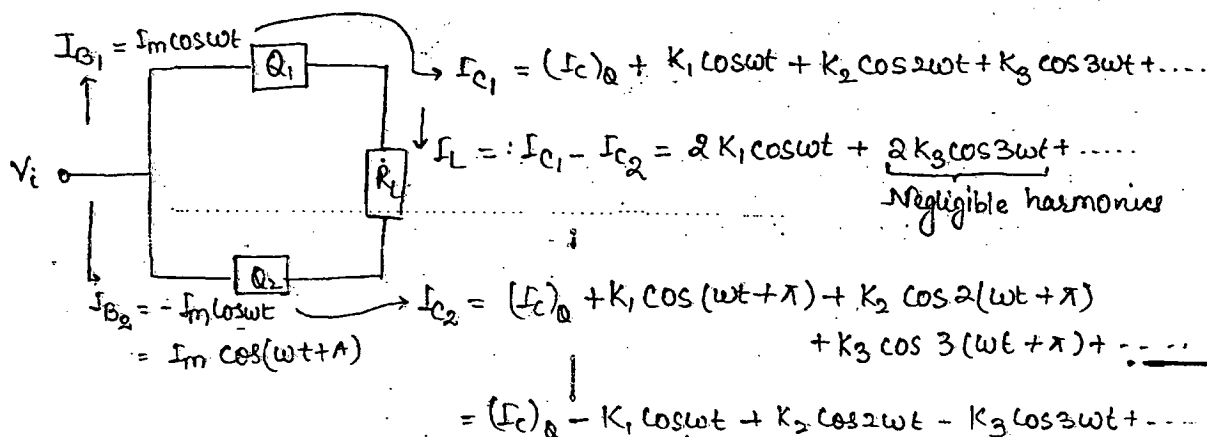
$$P_{Dmax} = \frac{4V_{CC}^2}{\pi^2 R_L'} - \frac{2V_{CC}^2}{\pi^2 R_L'} \Rightarrow \boxed{P_{Dmax} = \frac{2}{\pi^2} \cdot \frac{V_{CC}^2}{R_L'}}$$

$$\boxed{P_{acmax} = \frac{V_{CC}^2}{2R_L'}}$$

$$\boxed{P_{Dmax} = \frac{4}{\pi^2} P_{acmax}}$$

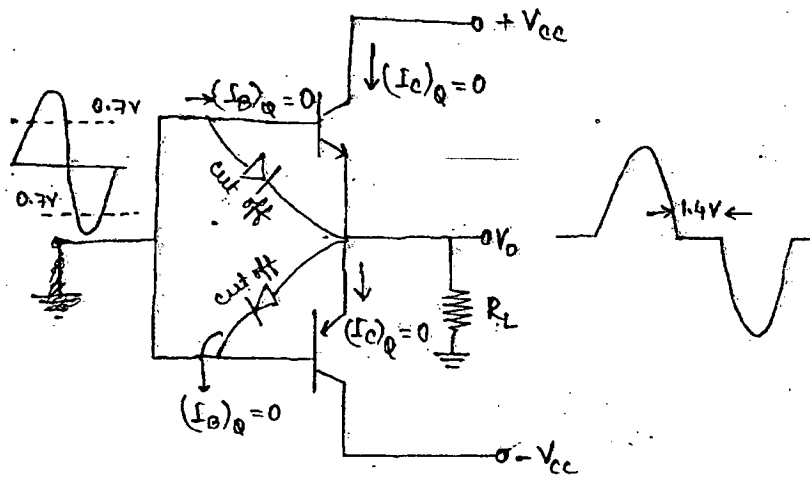


□ HARMONICS DISTORTION IN CLASS B.

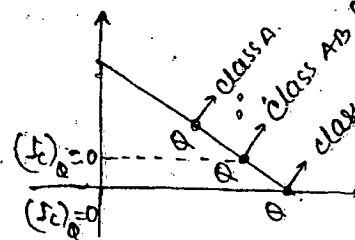
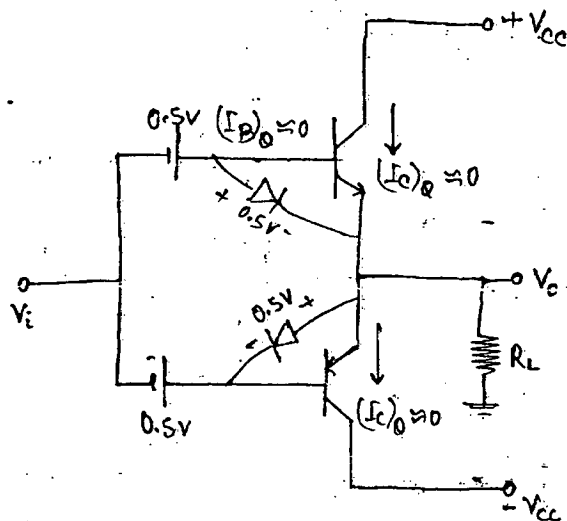


Even harmonic and dc are eliminated.

□ CROSS OVER DISTORTION IN CLASS B.



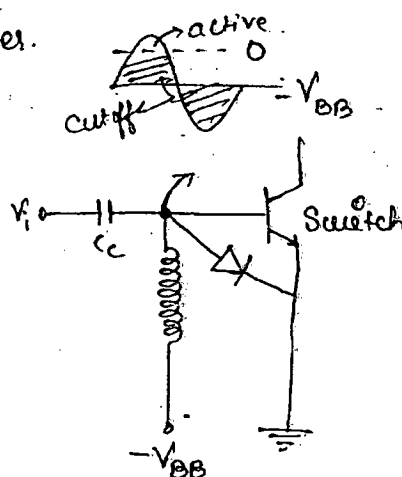
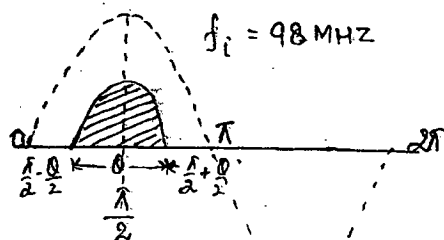
□ CLASS AB SYSTEM.



No di crossover distortion

□ CLASS C POWER AMPLIFIER (RADIO)

Tuned power Amplifier.



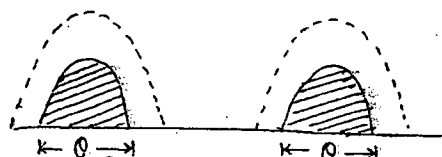
— On angle.

Transistor Q is in Saturation

$$P_D = (V_{CE})_Q \times (I_C)_Q$$

$$= 0 \times (I_C)_Q$$

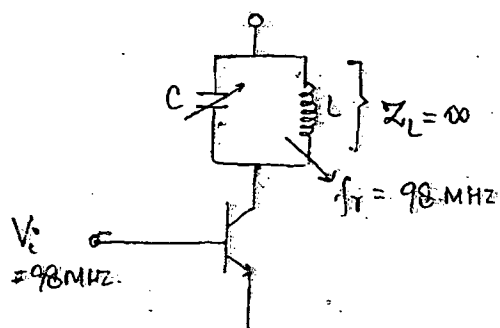
$$= 0$$



Fourier Series expansions

$$A_0 + \text{Fundamental} + 2^{\text{nd}} \text{ harmonic} + 3^{\text{rd}}$$

98 MHz.



Parallel Resonance

$$f_r = f_i$$

$$Z_L = \infty$$

$$A_V = A_I \frac{Z_L}{Z_i} = A_I \frac{\infty}{Z_i} = \infty$$

$$A_V = \infty$$

2π-Q

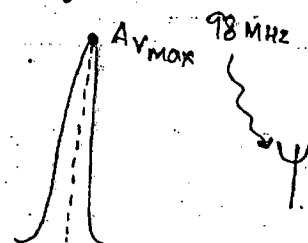
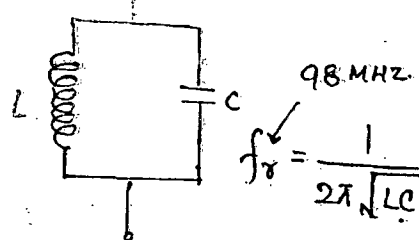
Transistor Q is cut off

$$P_D = (V_{CE})_Q \times (I_C)_Q$$

$$= V_{CC} \times 0$$

$$= 0$$

Band Pass



Final circuit.

